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(54) **DISPLAY UNIT AND METHOD OF
MANUFACTURING THE SAME**

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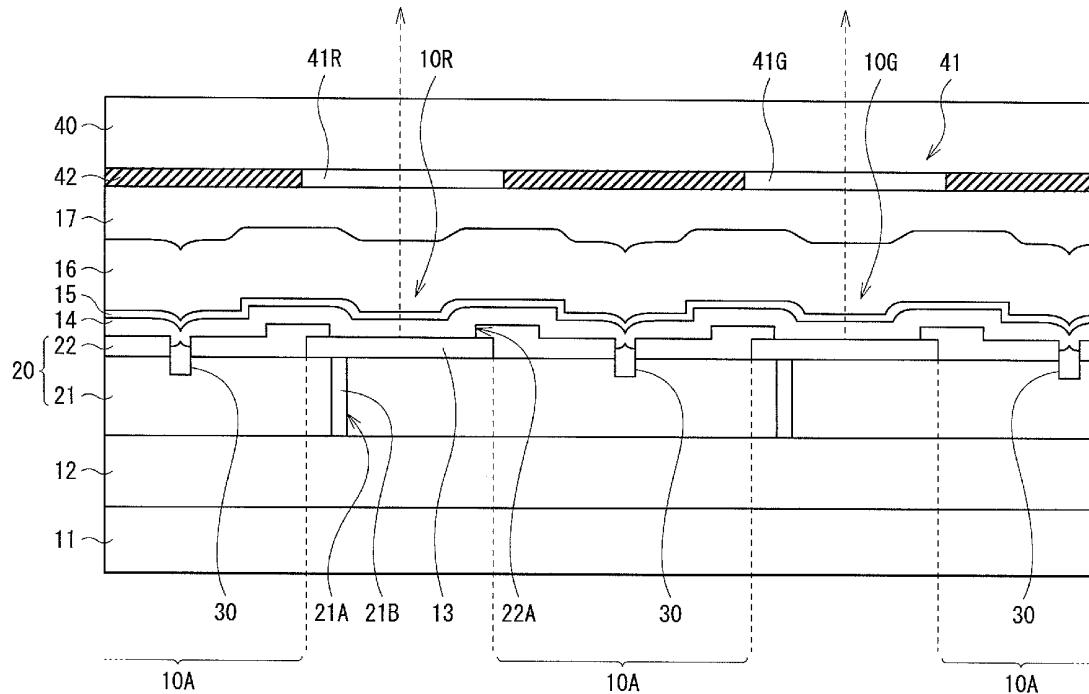
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257/E51.018

(57) **ABSTRACT**

A display unit includes, on a substrate, a plurality of organic EL devices, and an insulating film provided in an inter-device region between the plurality of organic EL devices, the insulating film including a groove in a position between the organic EL devices adjacent to each other.



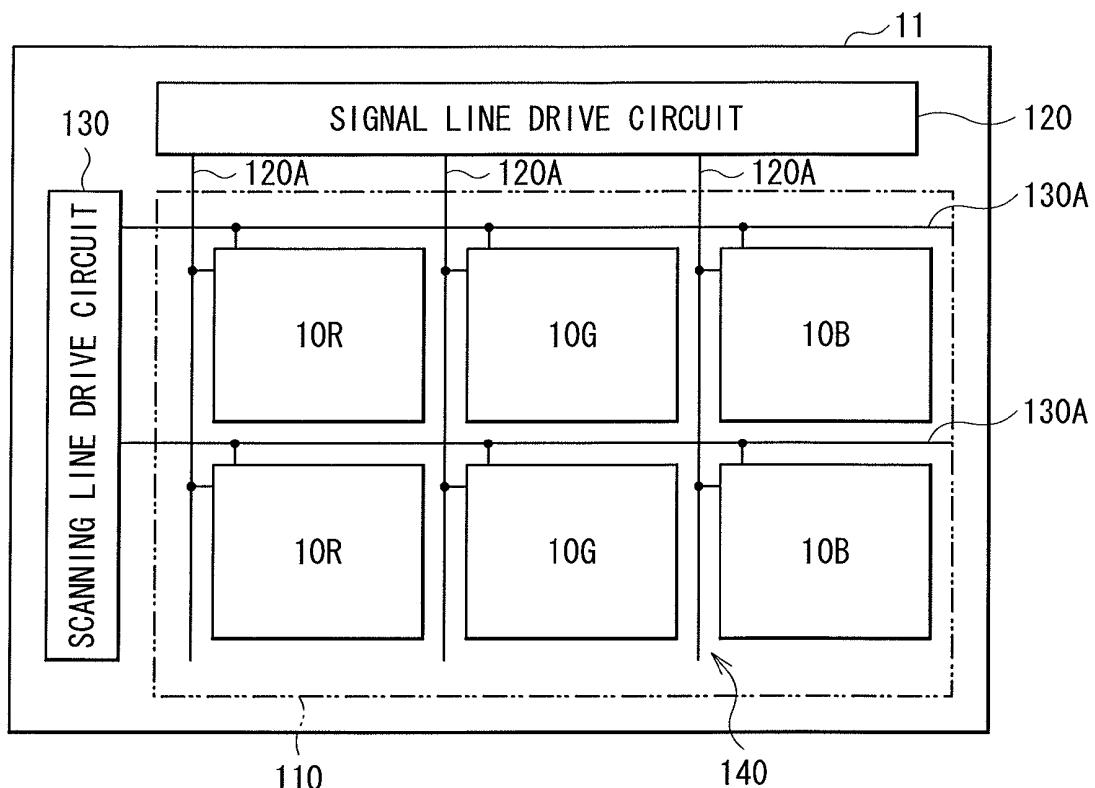


FIG. 1

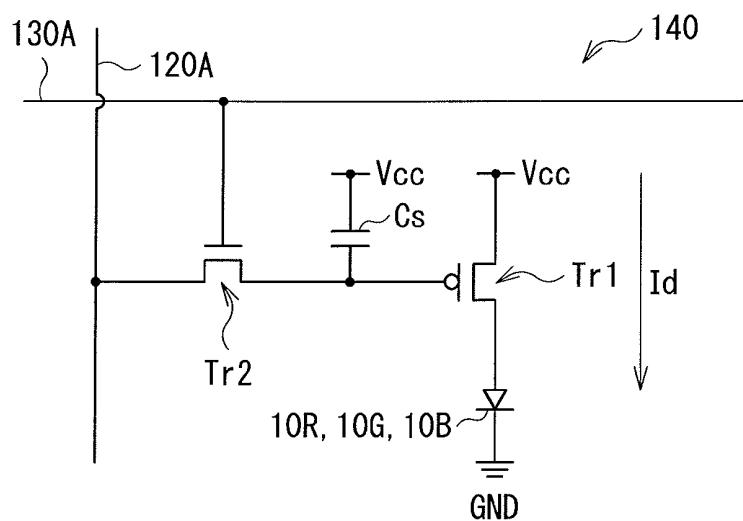
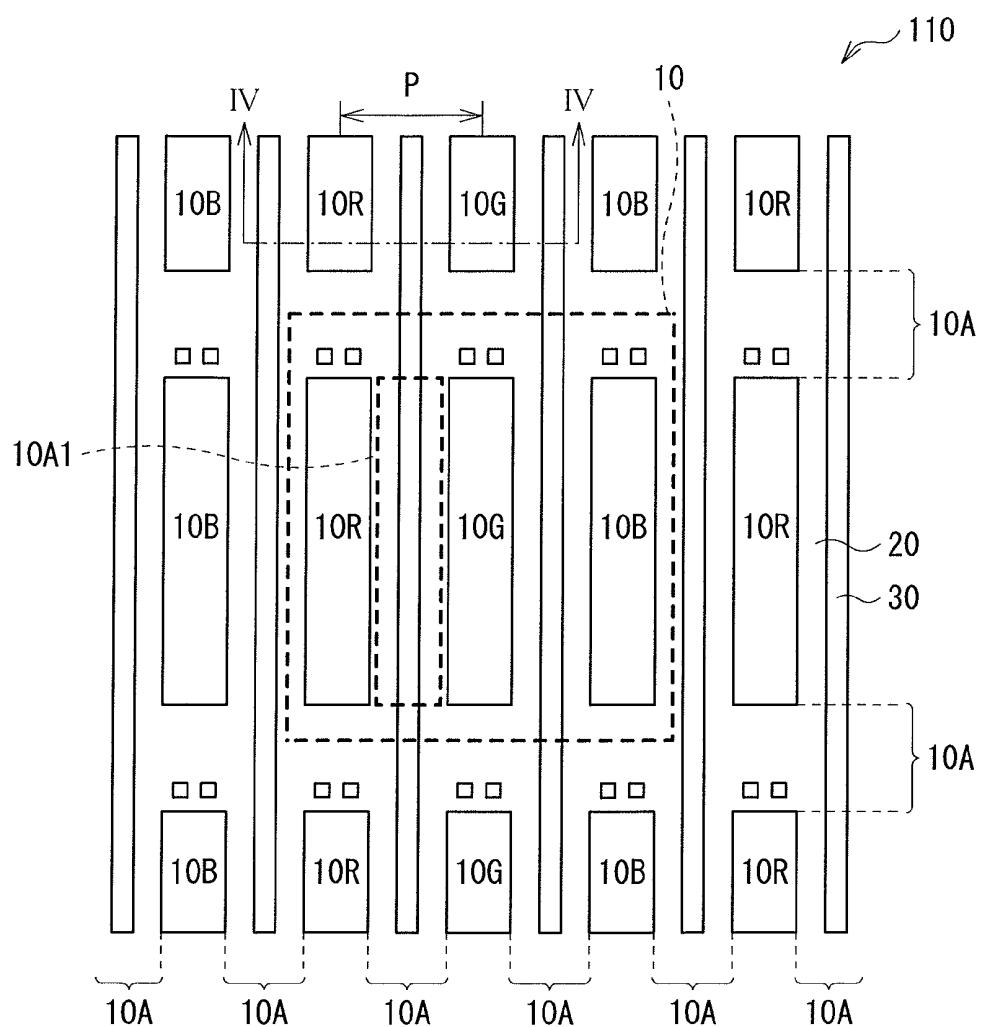


FIG. 2



COLUMN DIRECTION

 LAMINATION DIRECTION ROW DIRECTION

FIG. 3

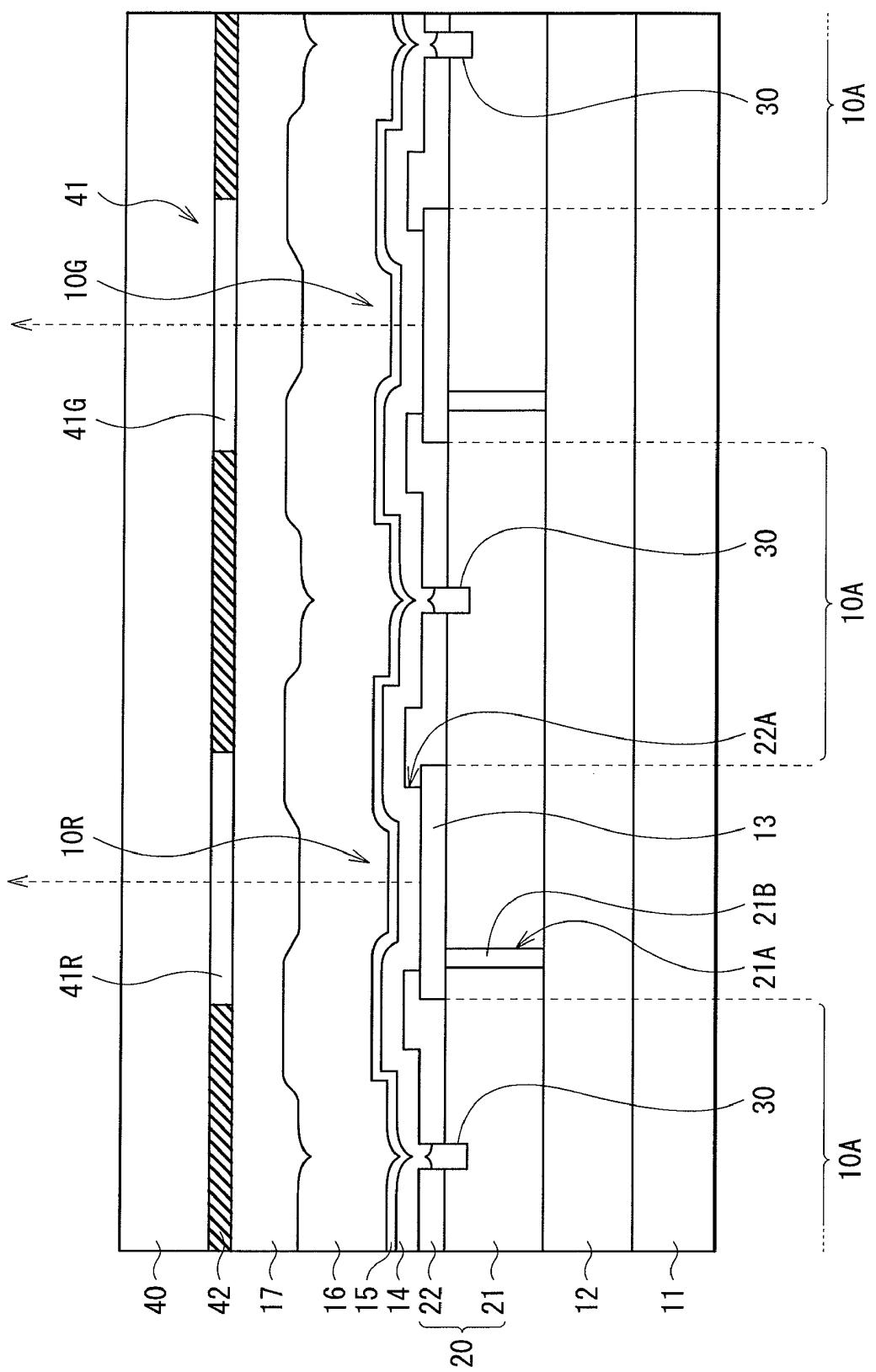


FIG. 4

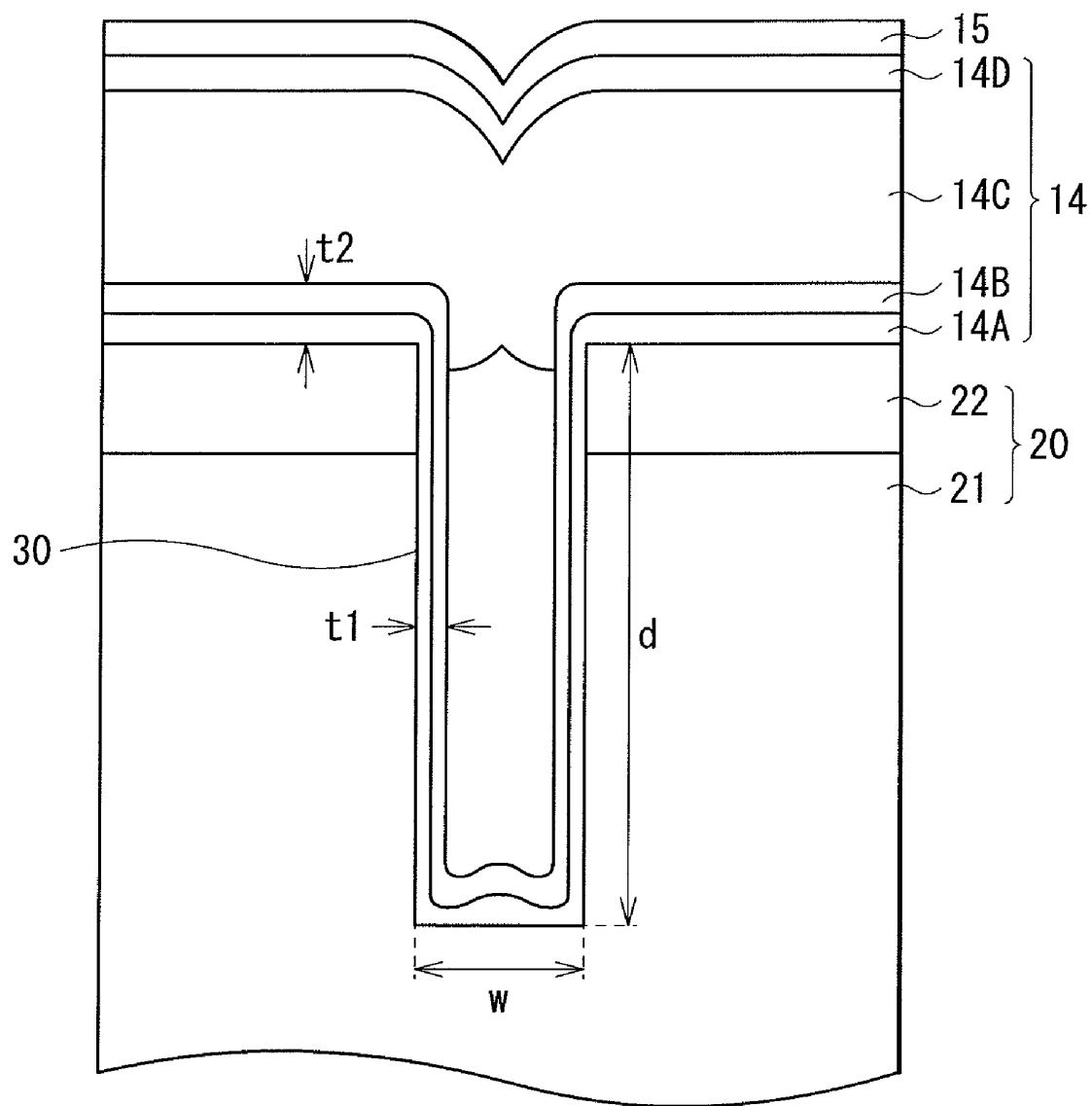


FIG. 5

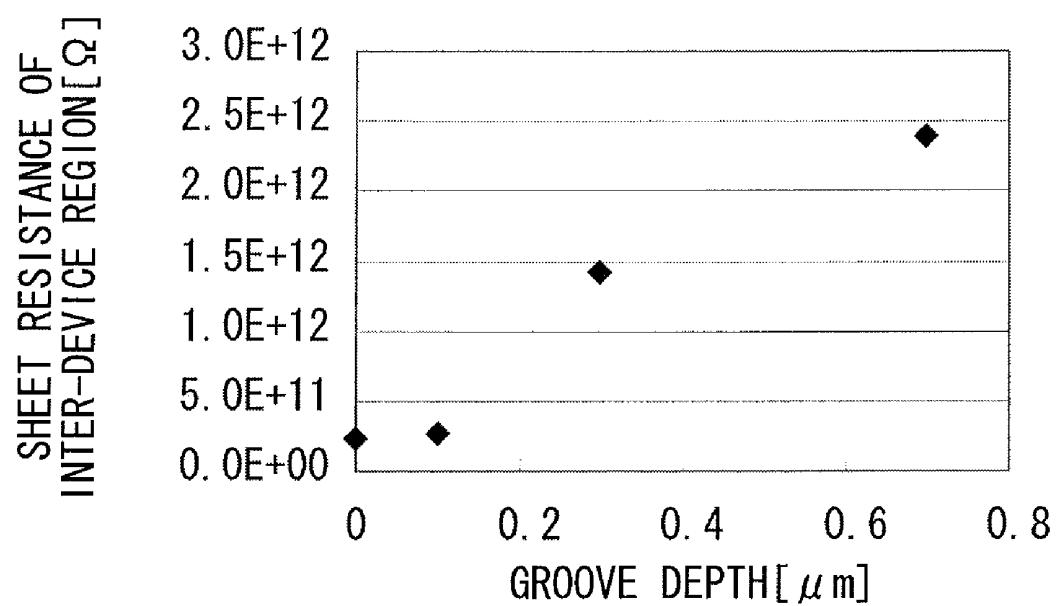


FIG. 6

FIG. 7A

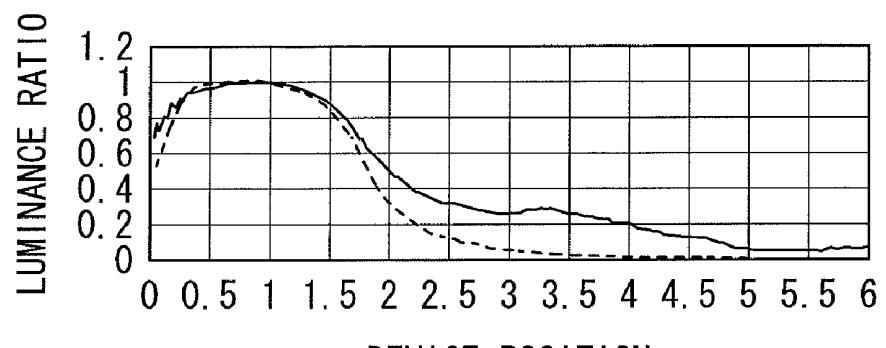


FIG. 7B

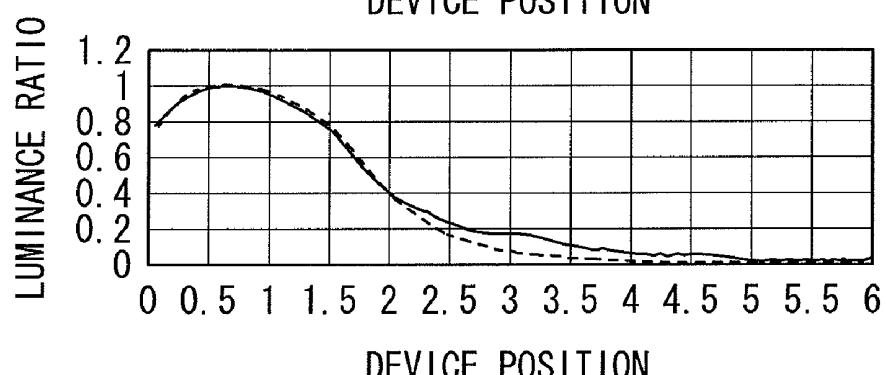


FIG. 7C

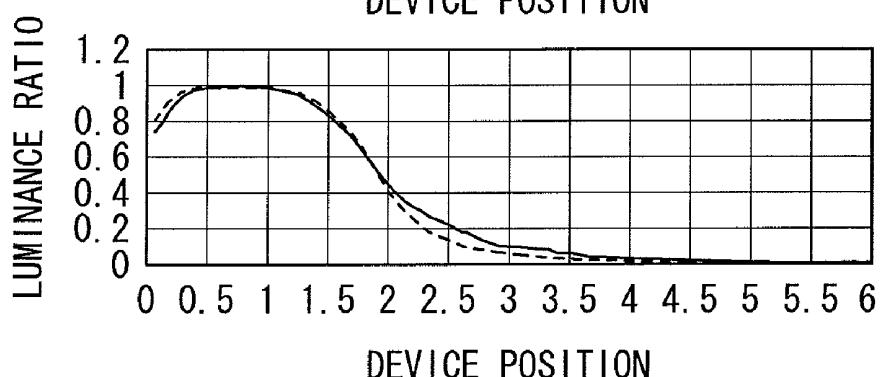


FIG. 8A

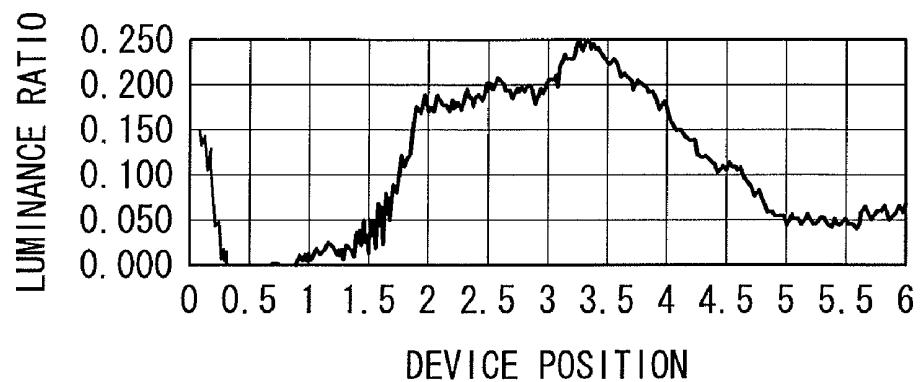


FIG. 8B

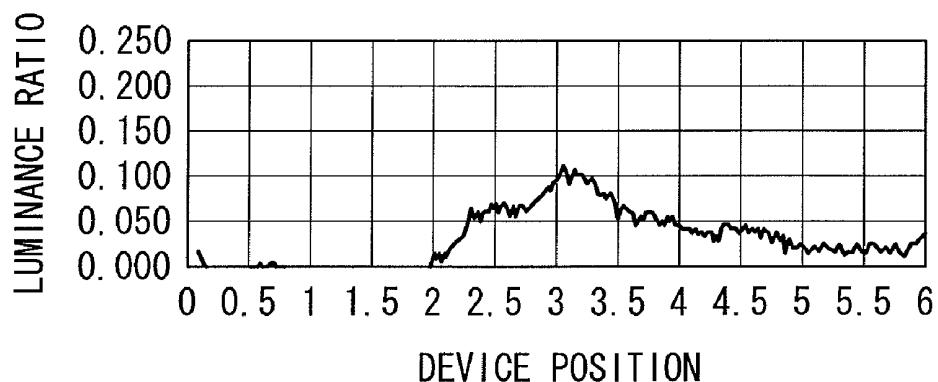
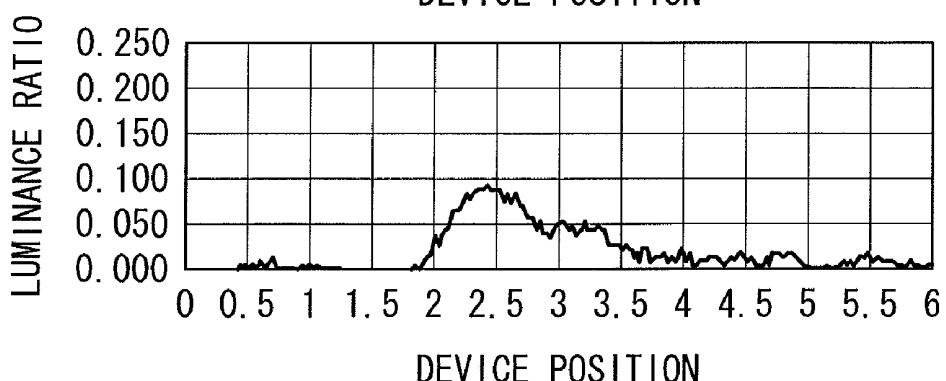


FIG. 8C



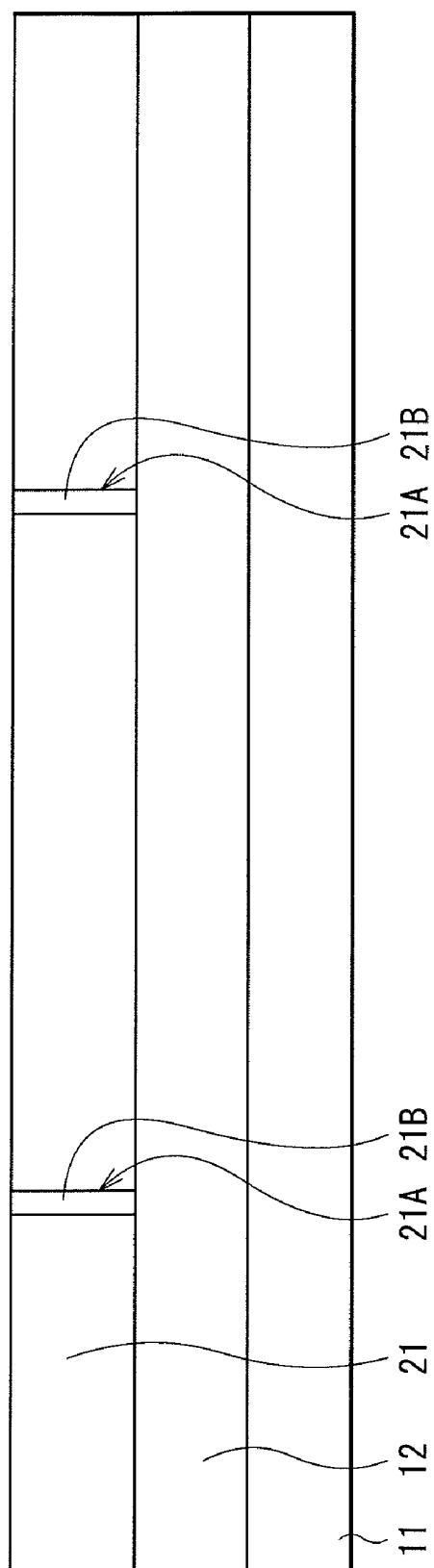


FIG. 9

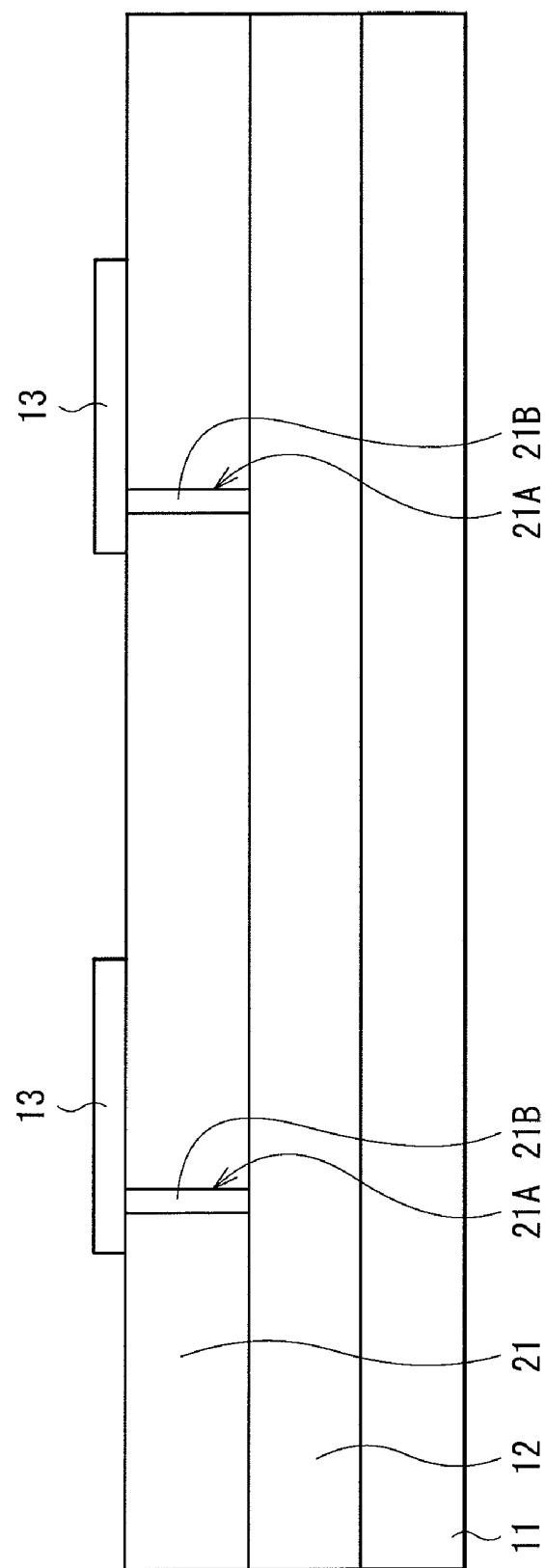


FIG. 10

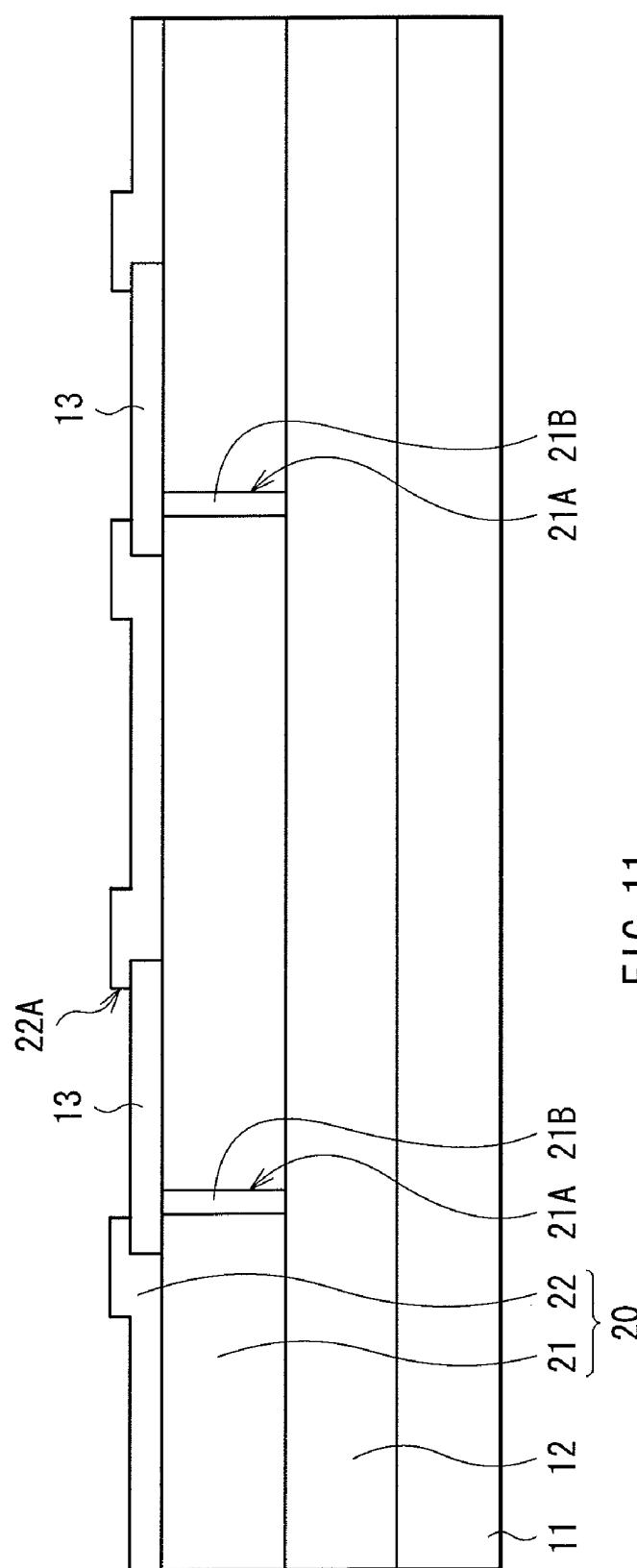


FIG. 11

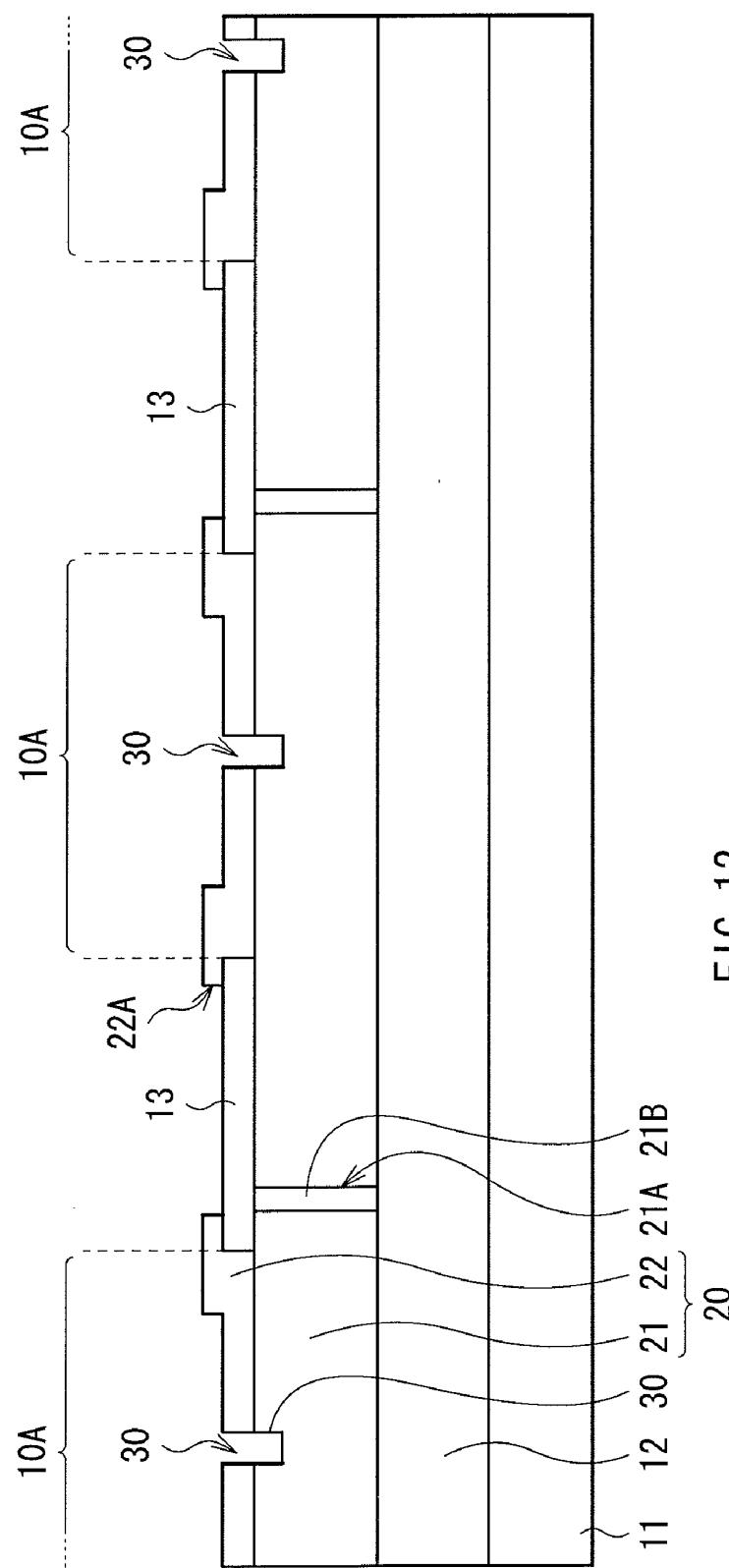


FIG. 12

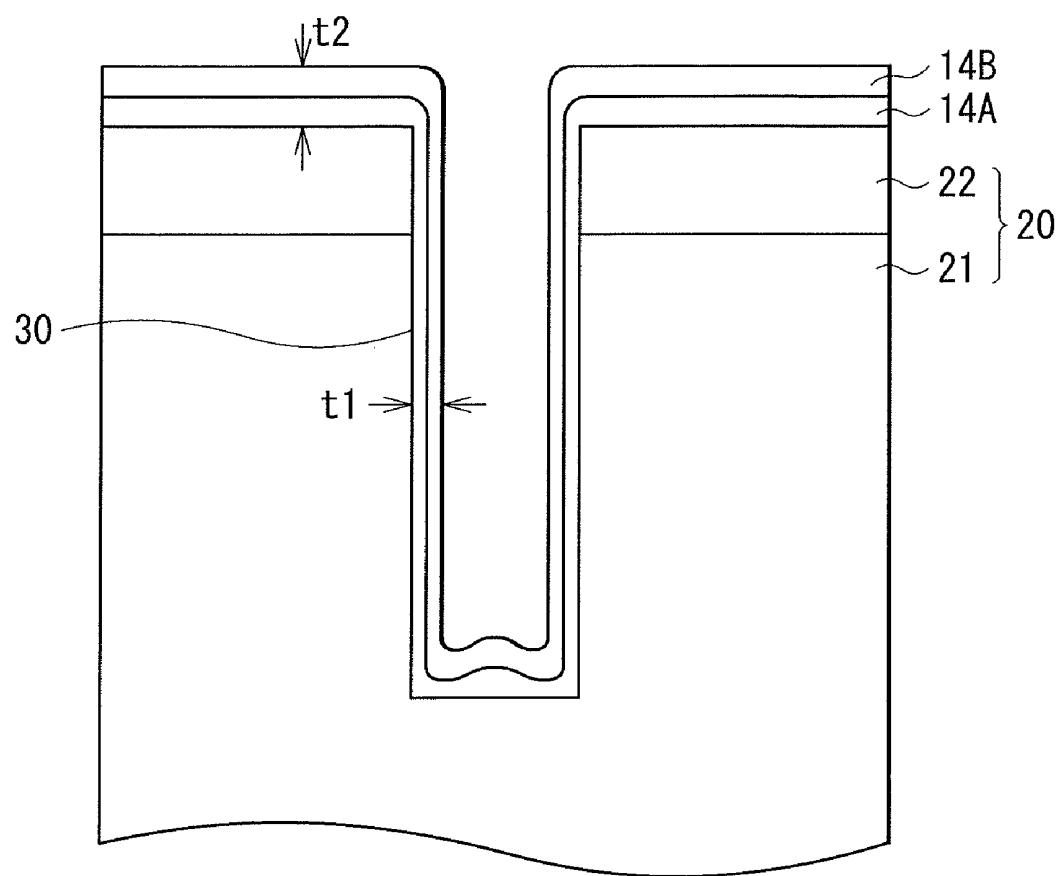


FIG. 13

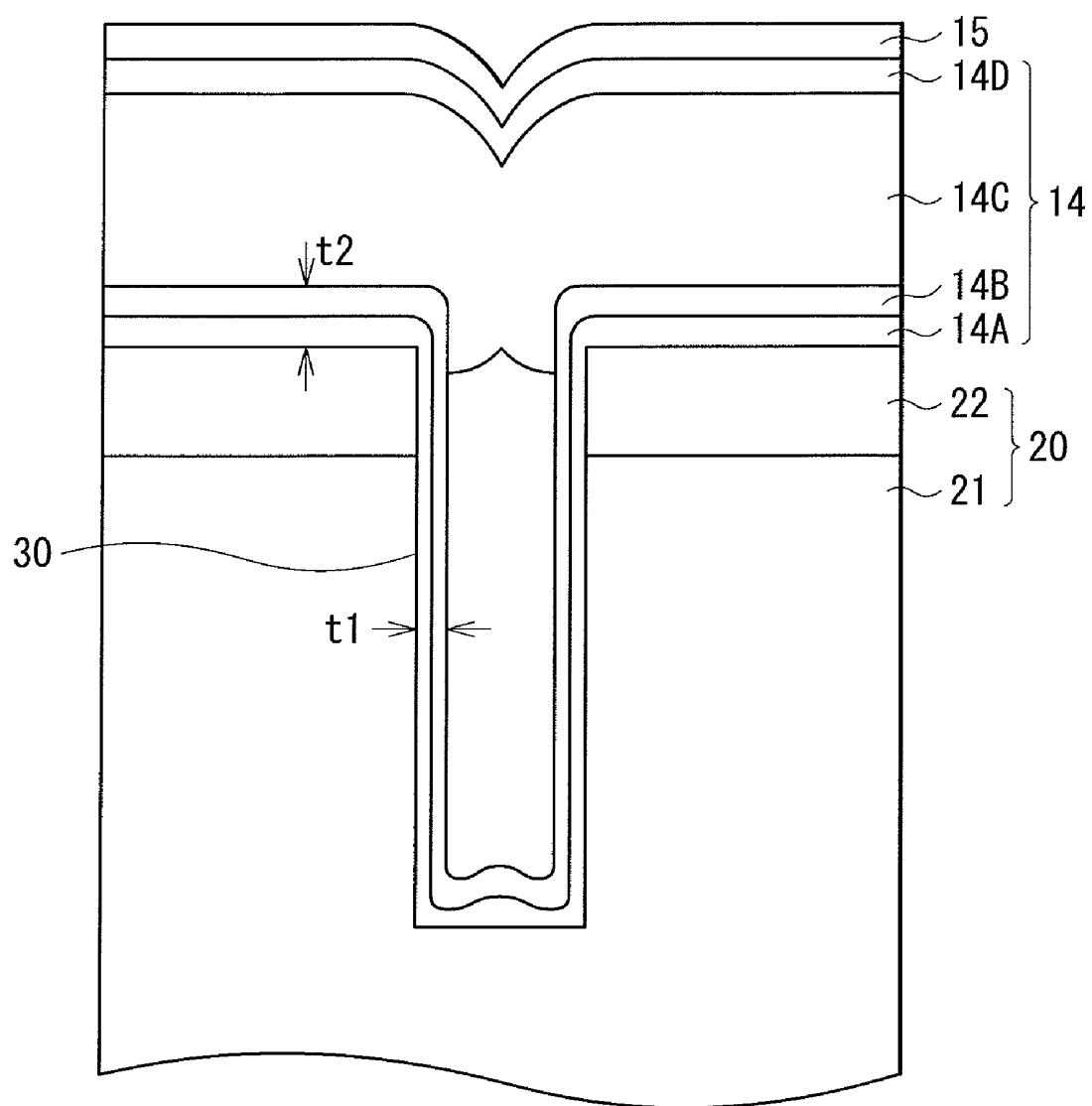


FIG. 14

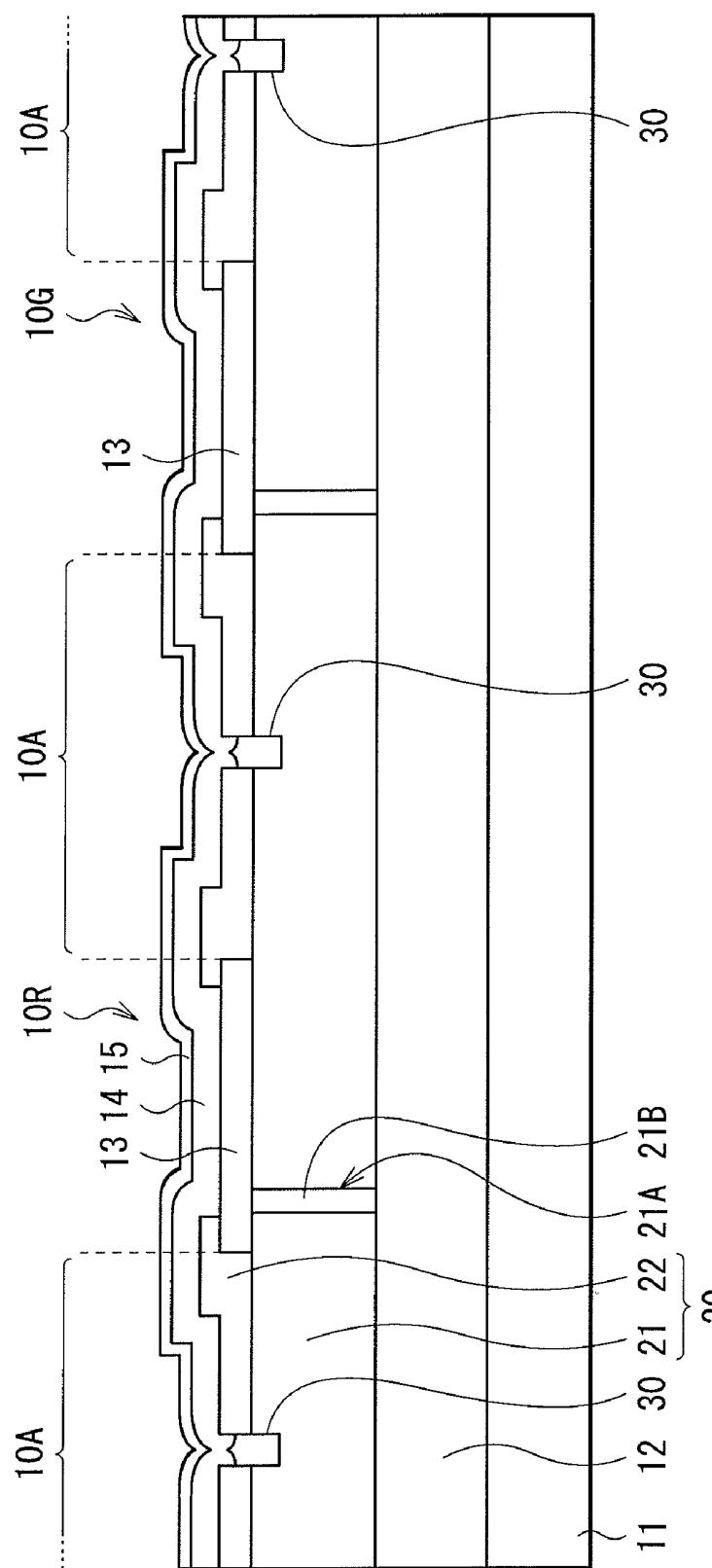


FIG. 15

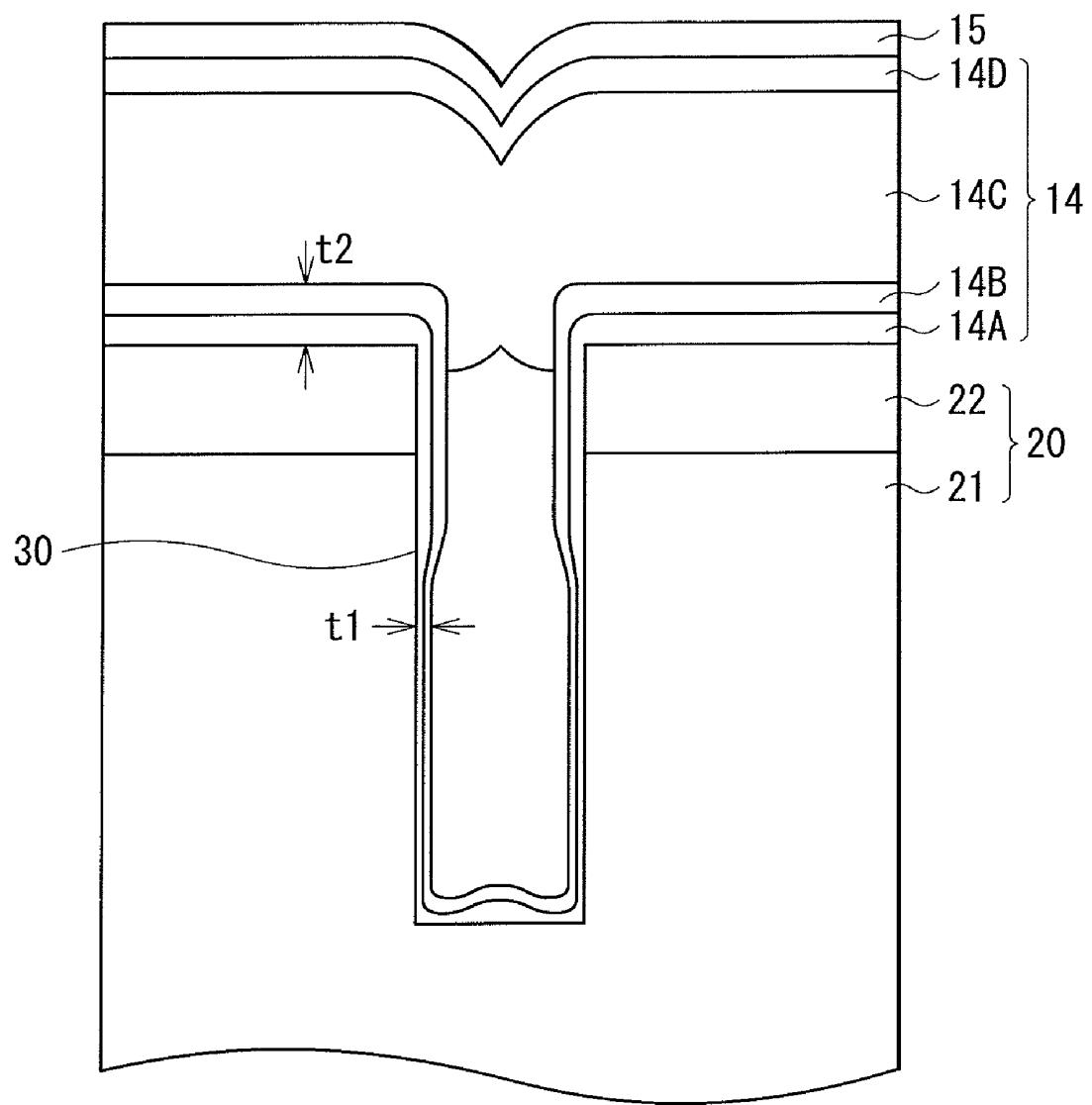


FIG. 16

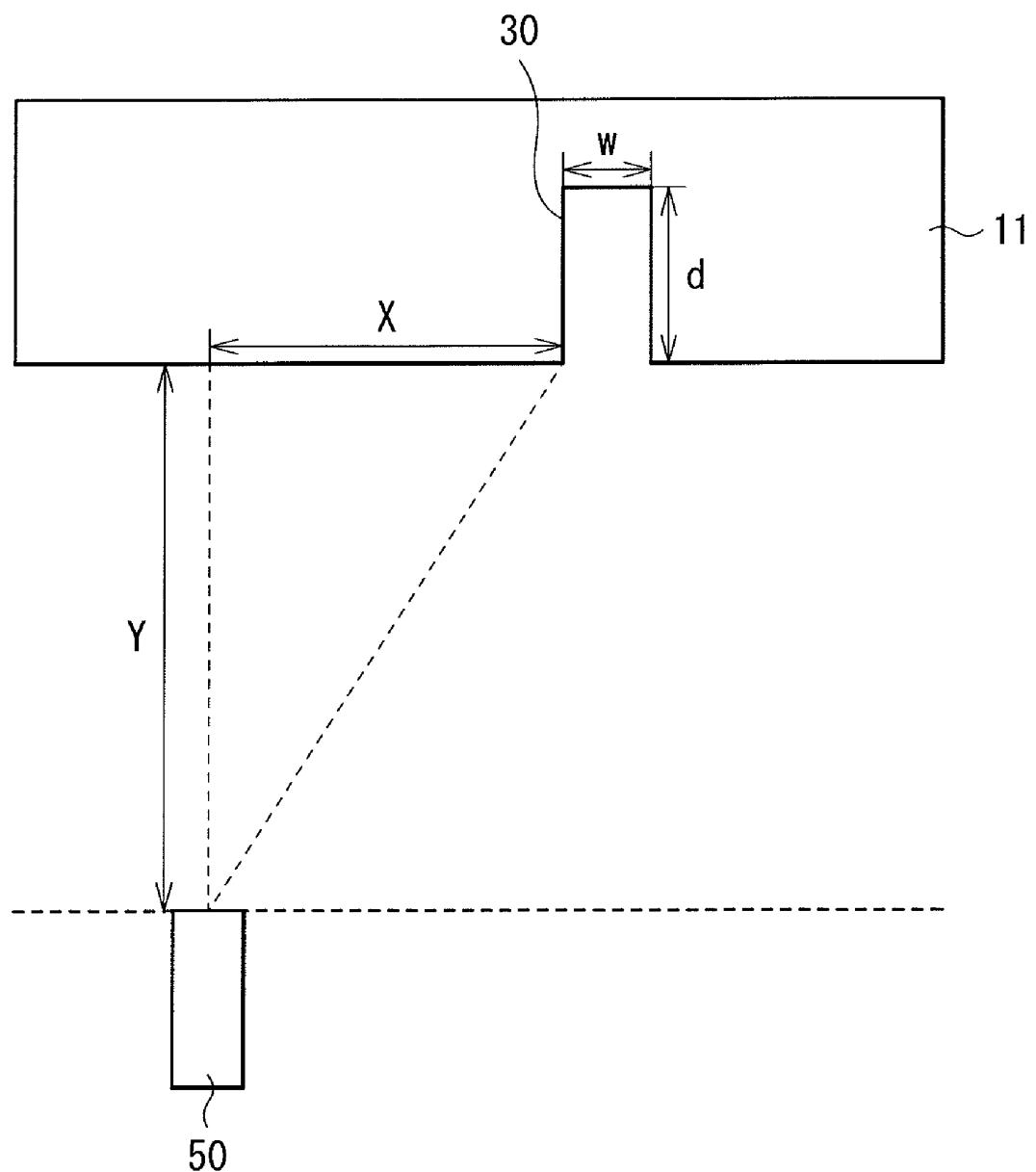


FIG. 17

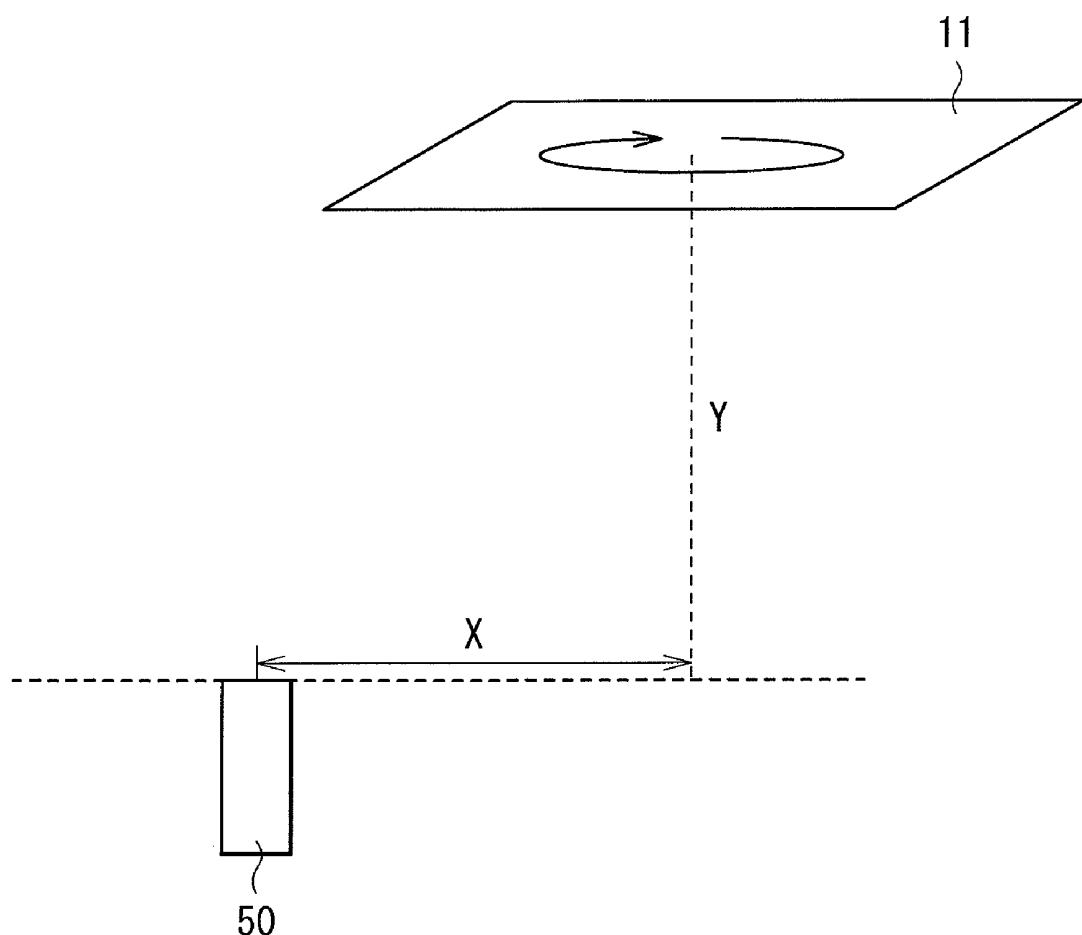


FIG. 18

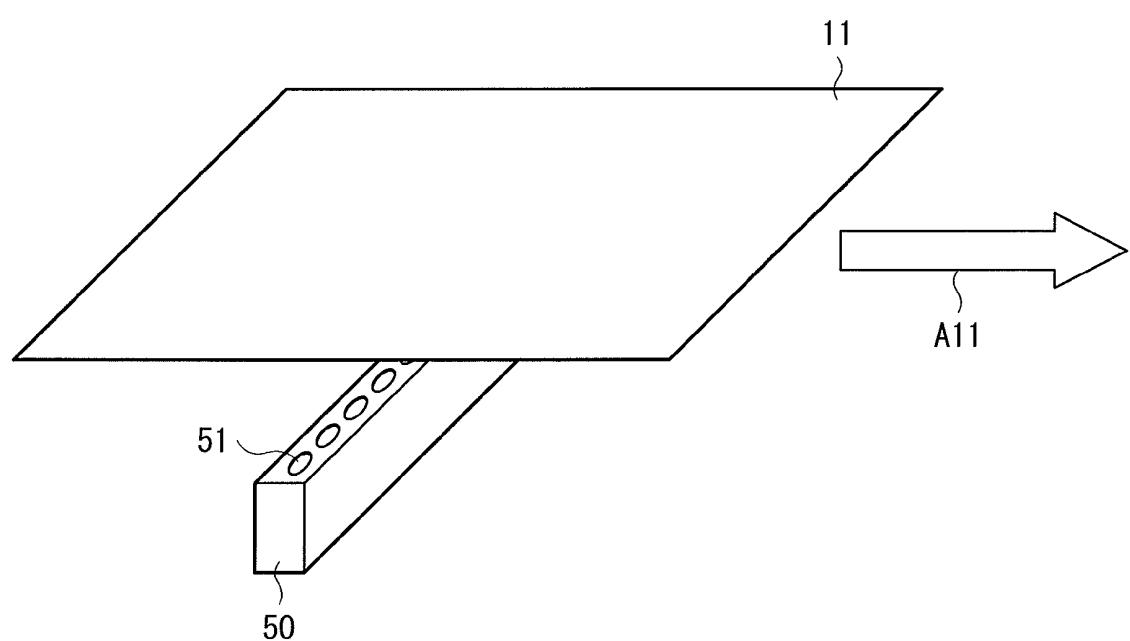


FIG. 19

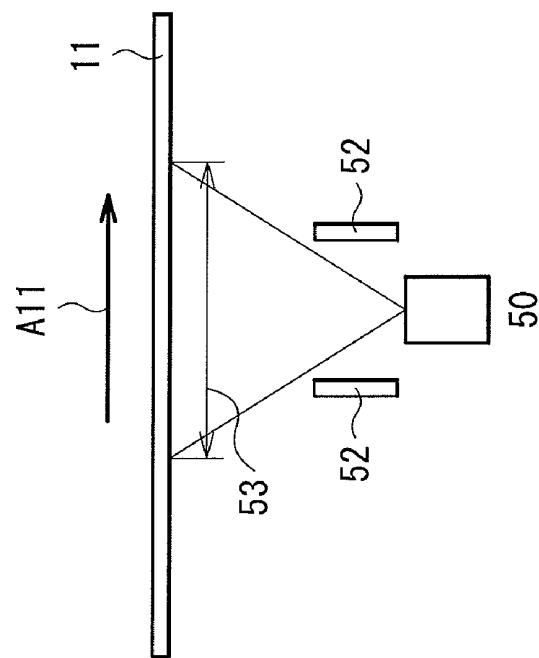
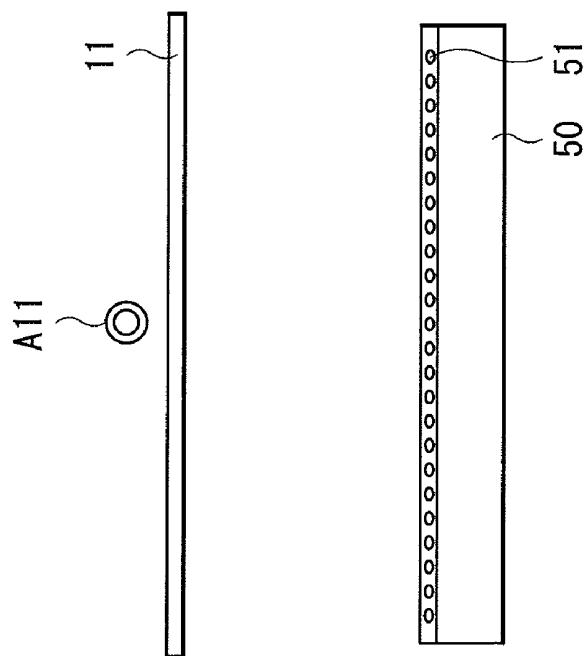


FIG. 20



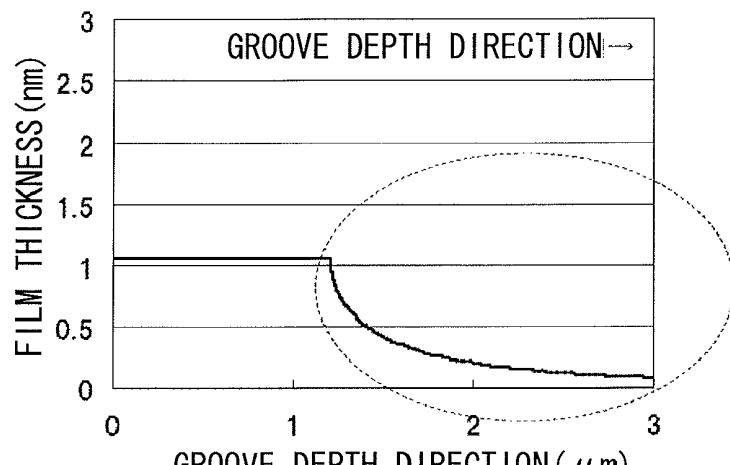


FIG. 21

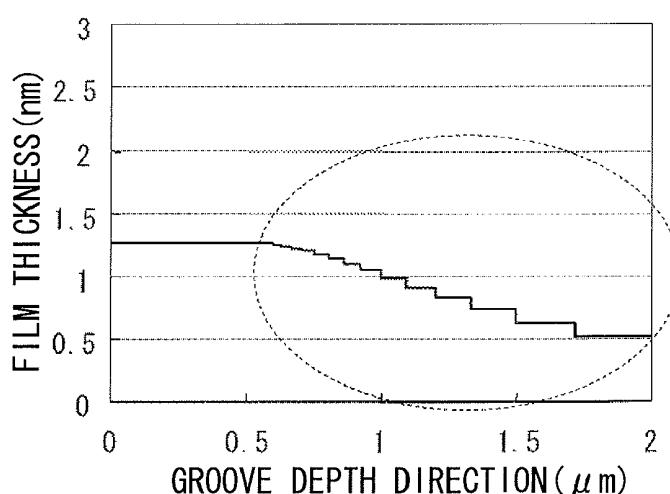


FIG. 22

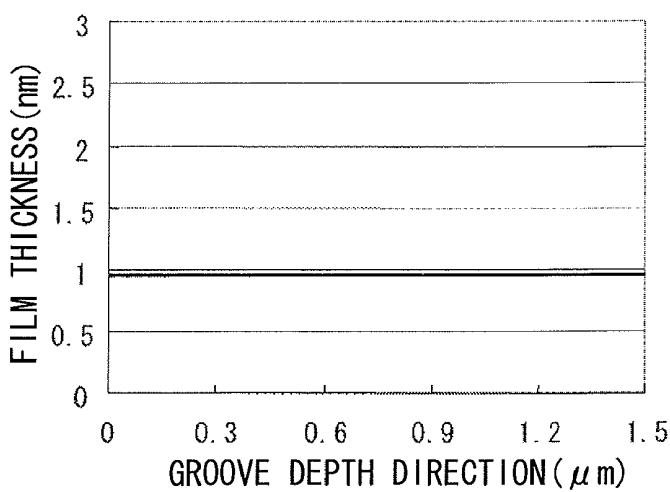
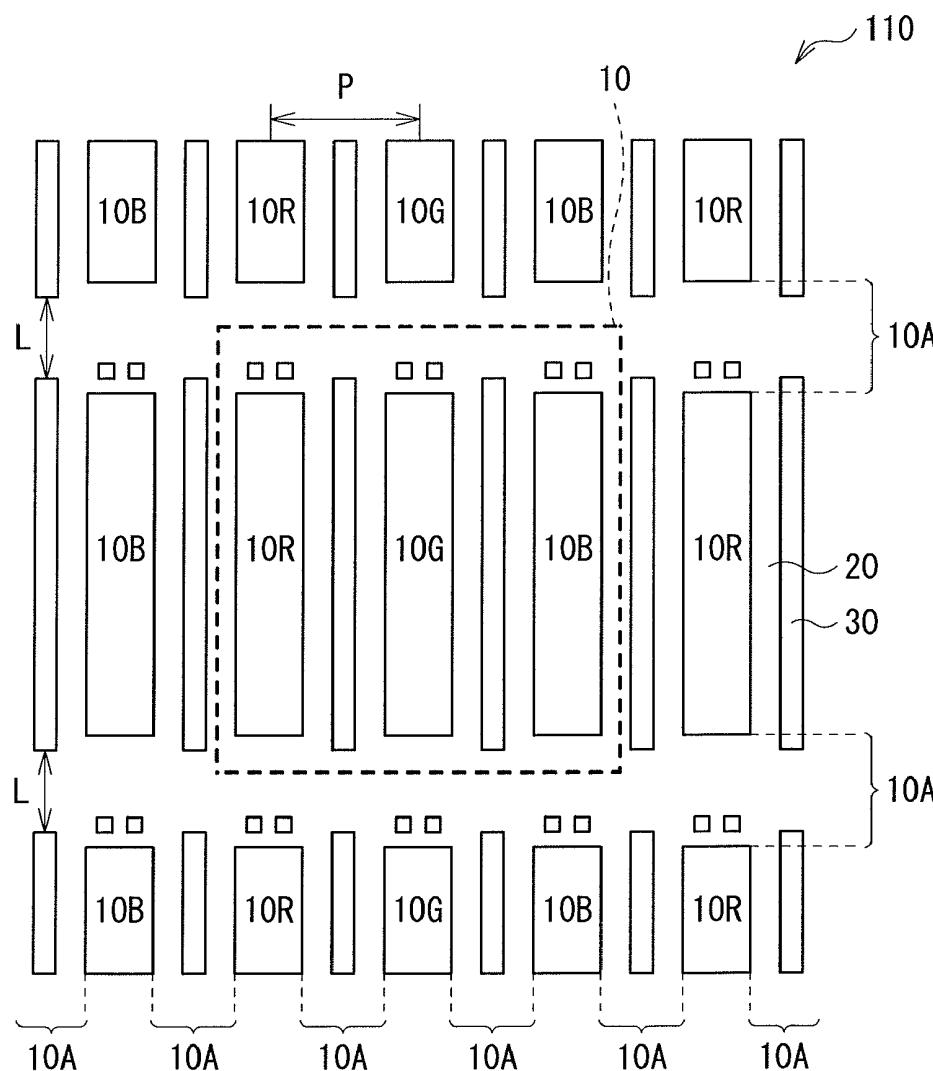


FIG. 23



COLUMN DIRECTION
↑
LAMINATION DIRECTION → ROW DIRECTION

FIG. 24

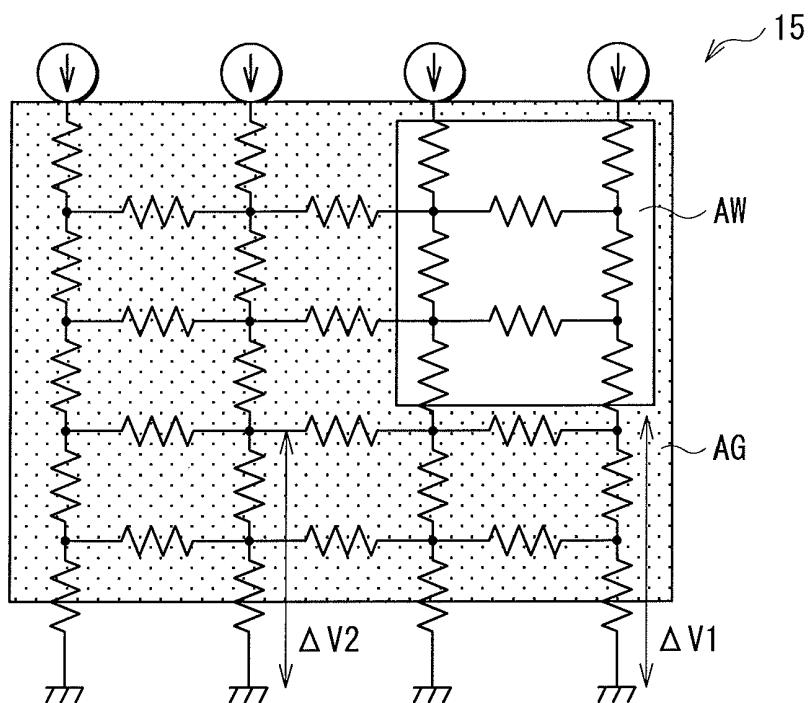


FIG. 25

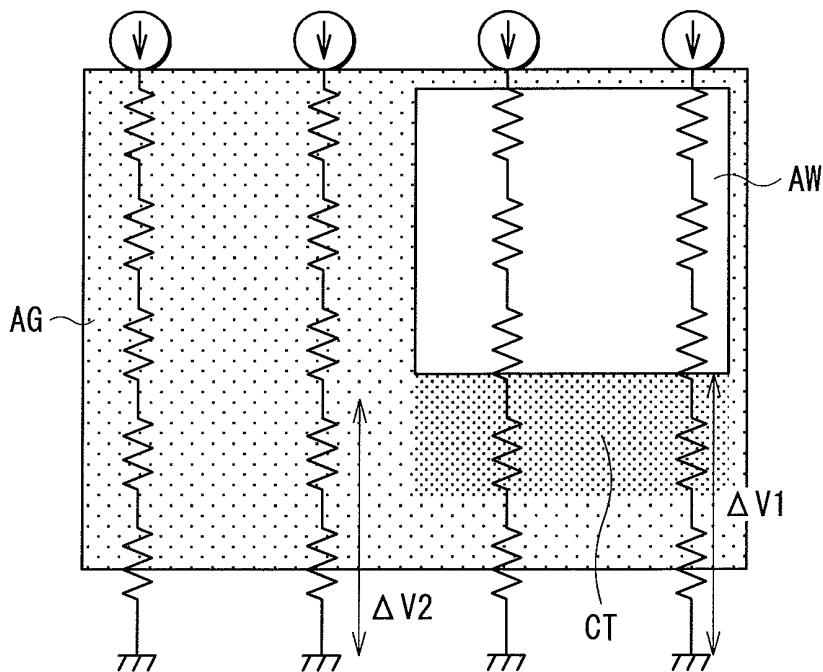


FIG. 26

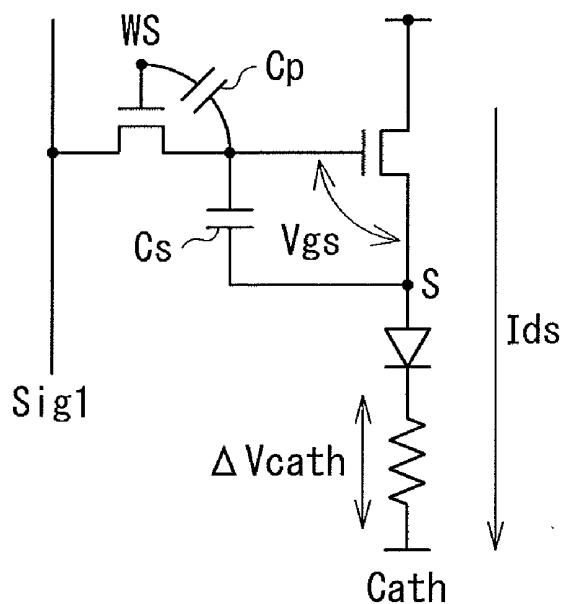


FIG. 27

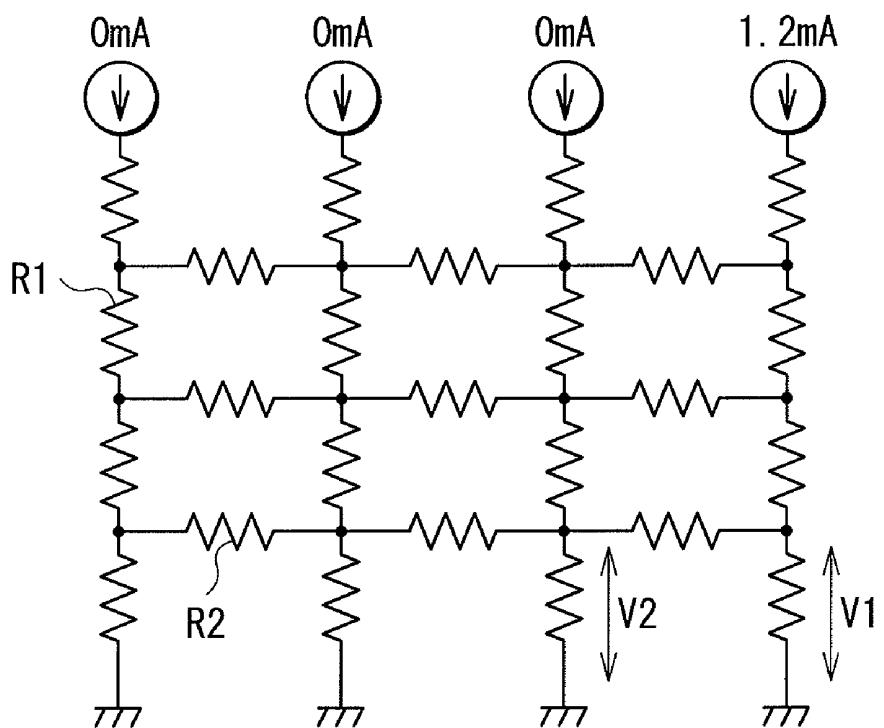


FIG. 28

MgAg THICKNESS	R1	FIRST EMBODIMENT	THIRD EMBODIMENT	
			L=1.0 μ m	L=2.4 μ m
4nm	85 Ω	∞	743 Ω	310 Ω
5nm	42. 5 Ω	∞	371. 5 Ω	155 Ω
6nm	28. 33 Ω	∞	247. 67 Ω	103. 33 Ω
7nm	21. 25 Ω	∞	185. 75 Ω	77. 5 Ω
8nm	18. 21 Ω	∞	159. 21 Ω	66. 43 Ω

FIG. 29

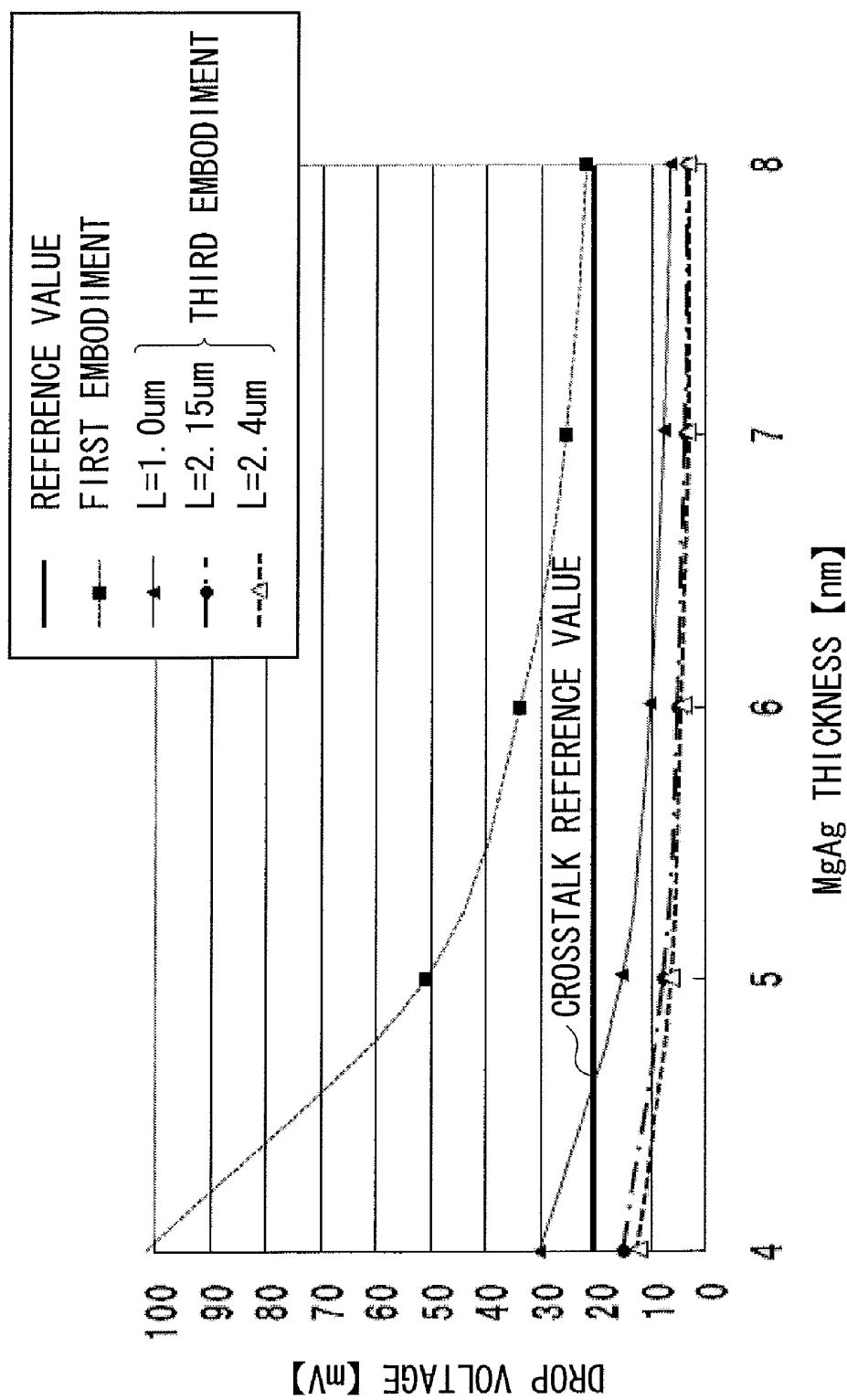


FIG. 30

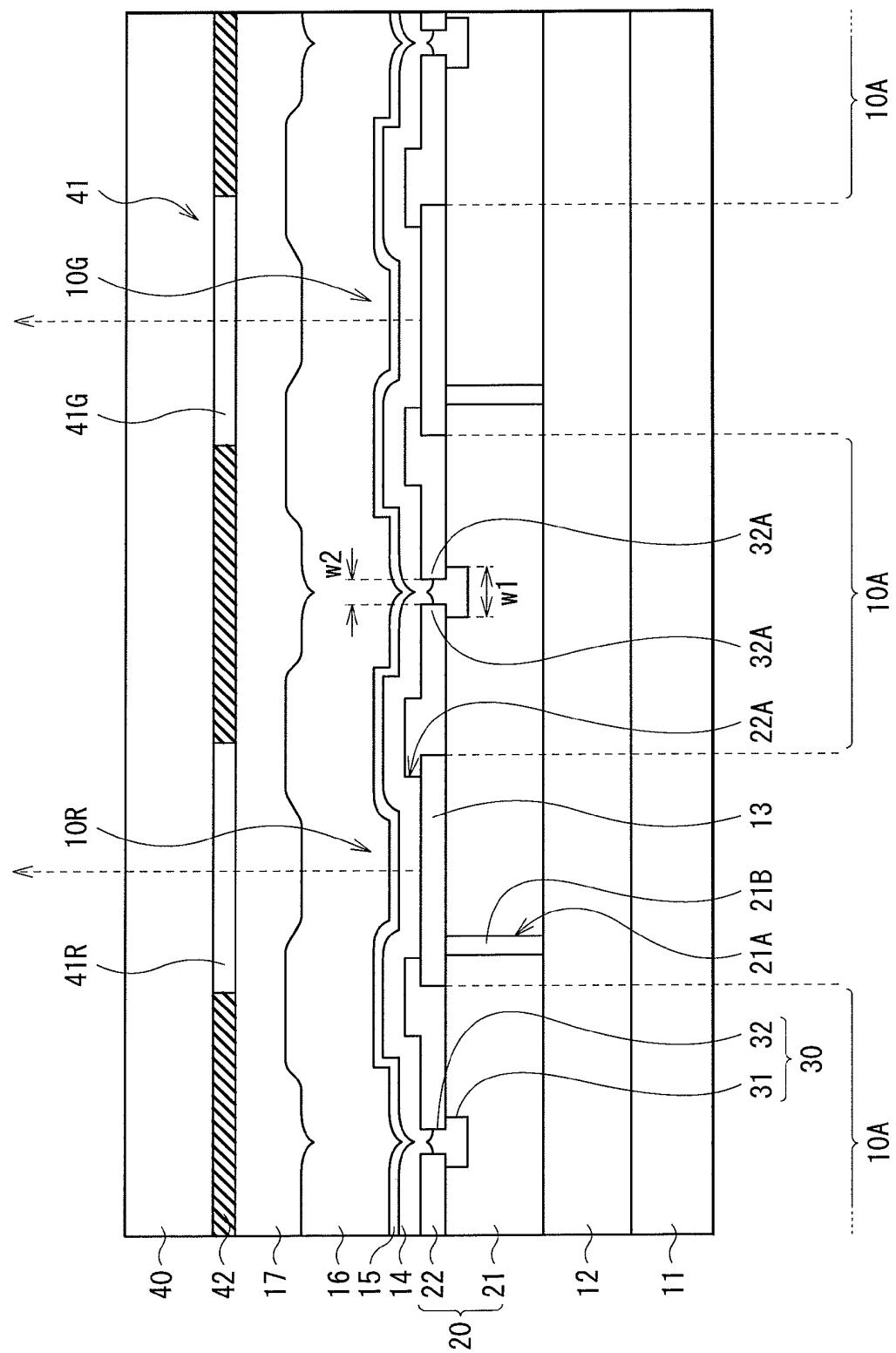


FIG. 31

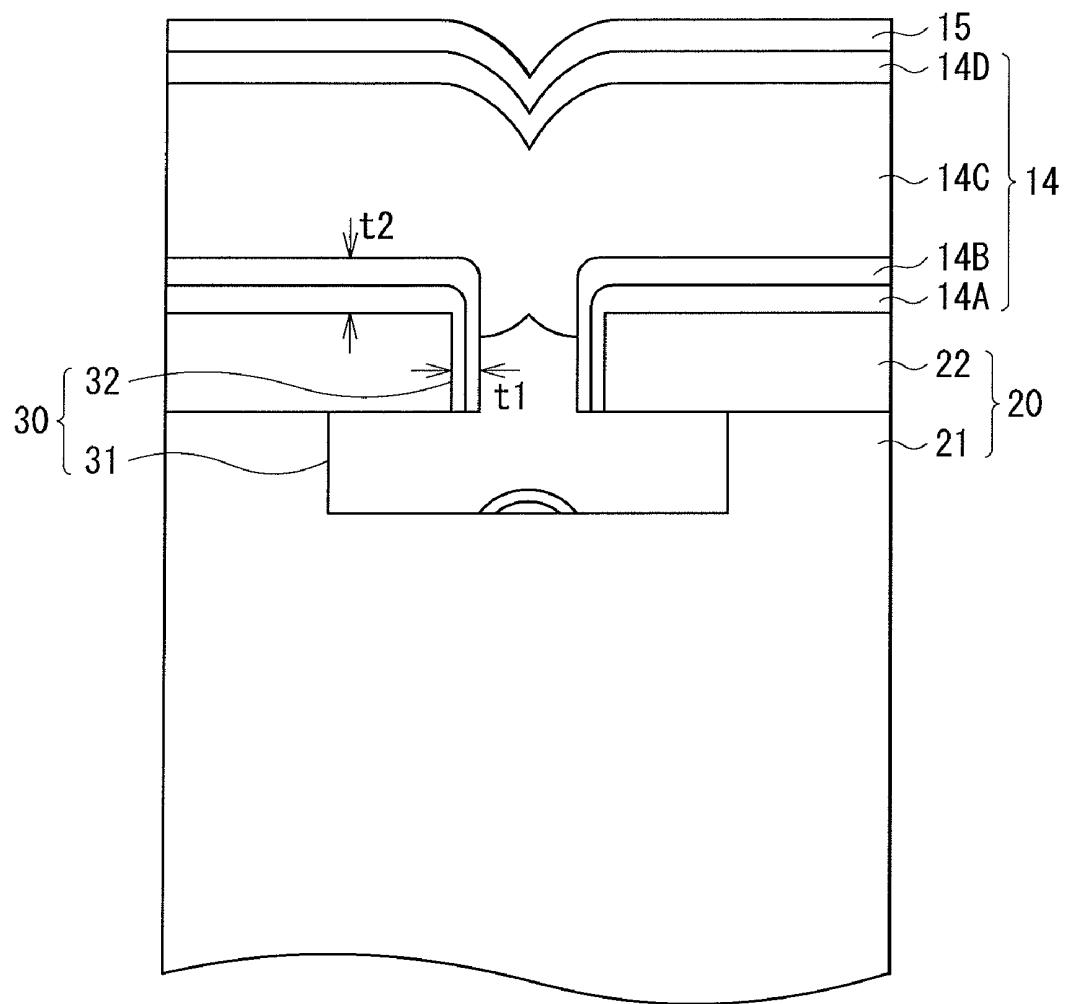


FIG. 32

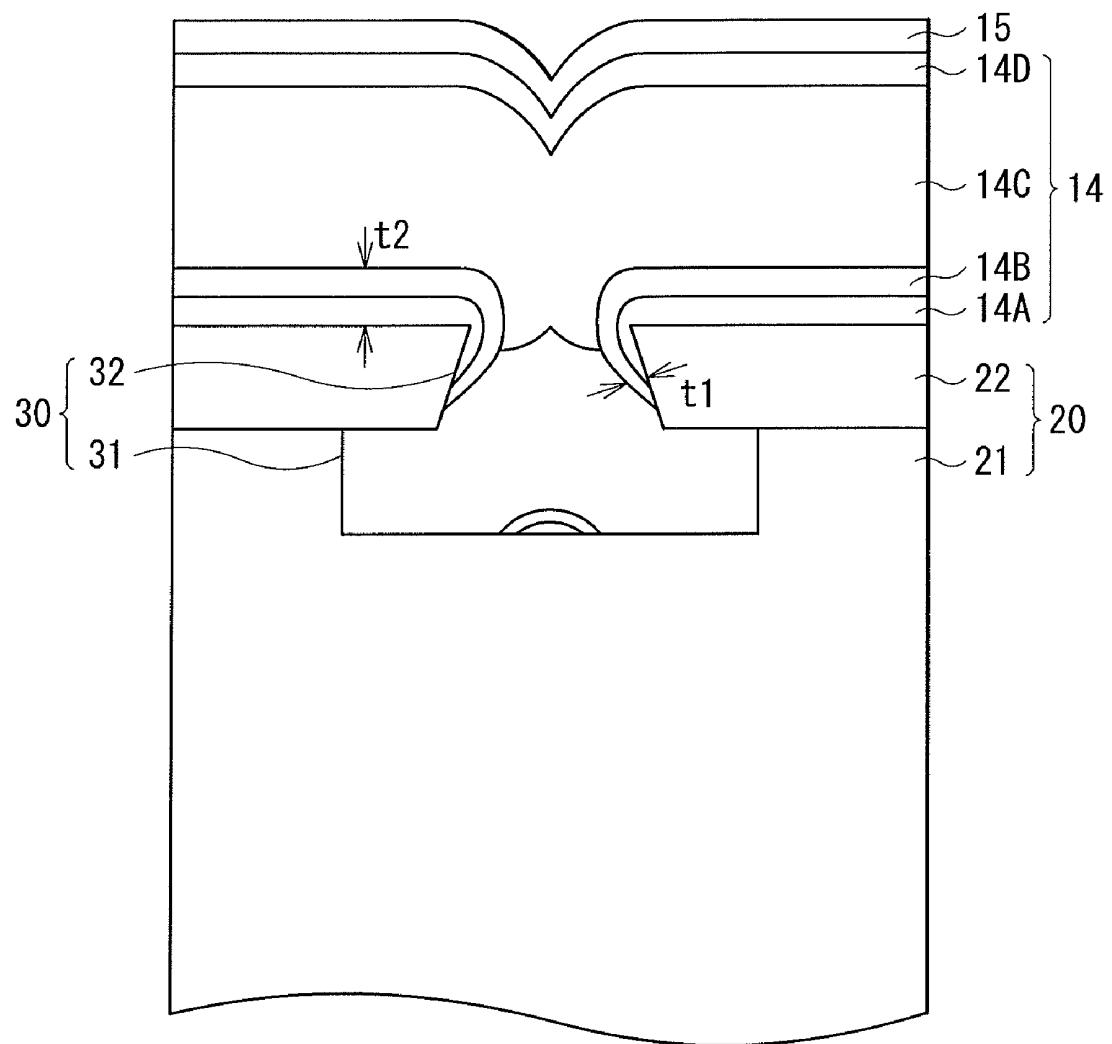


FIG. 33

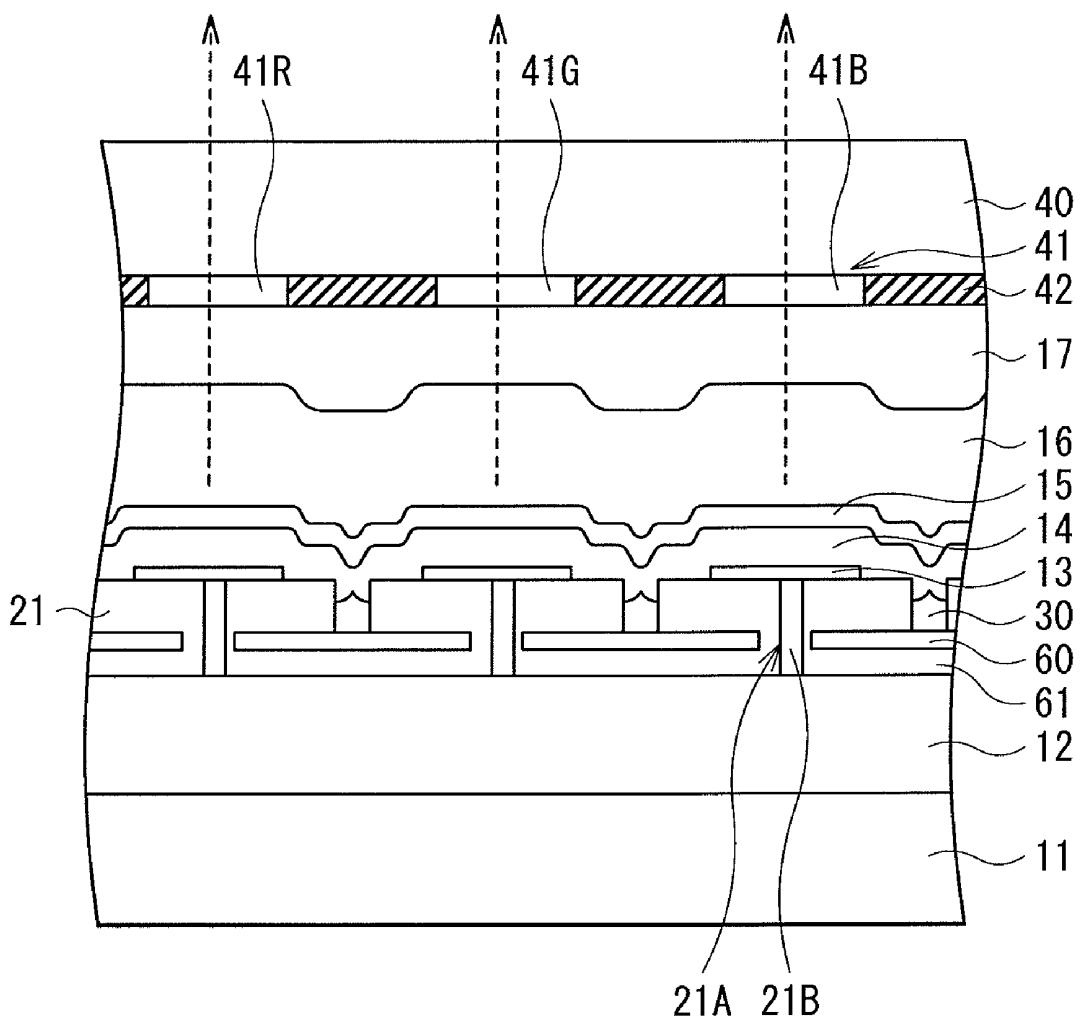


FIG. 34

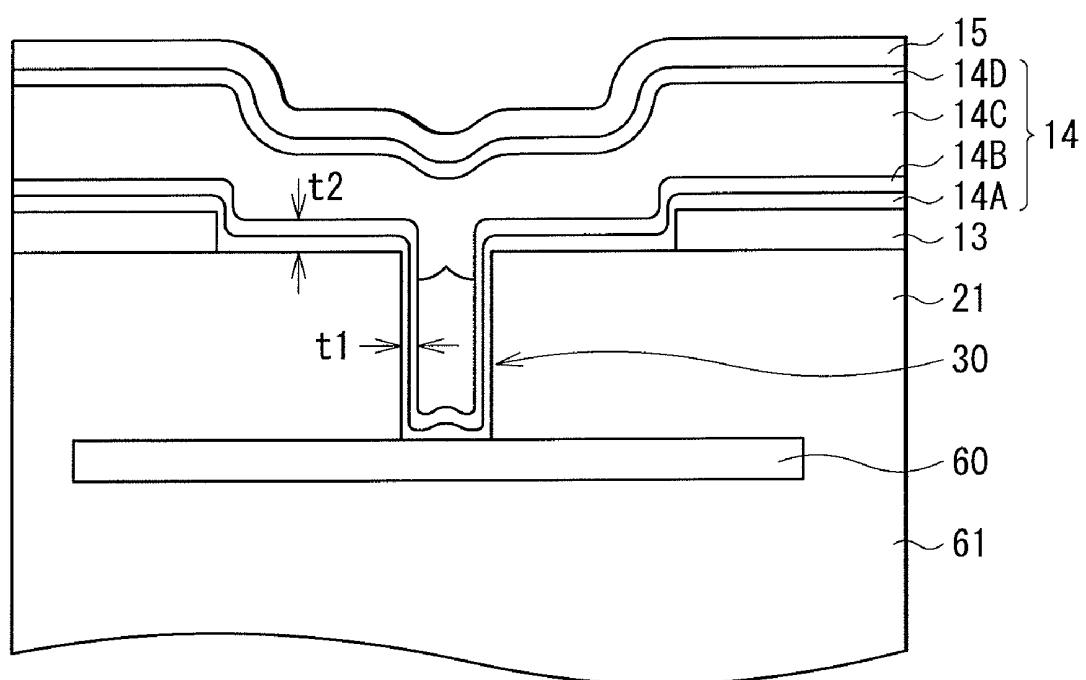


FIG. 35

FIG. 36A

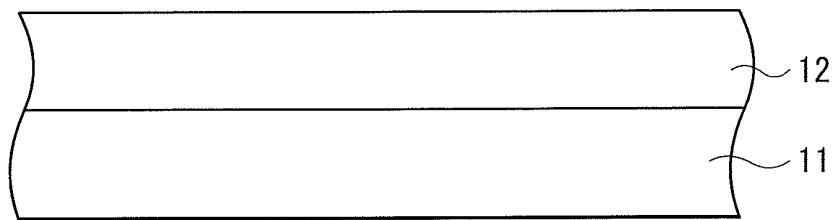


FIG. 36B

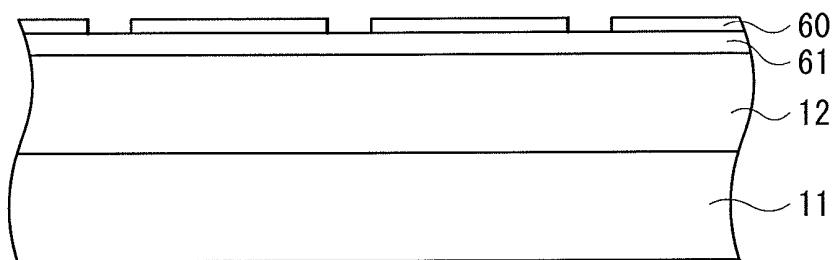


FIG. 36C

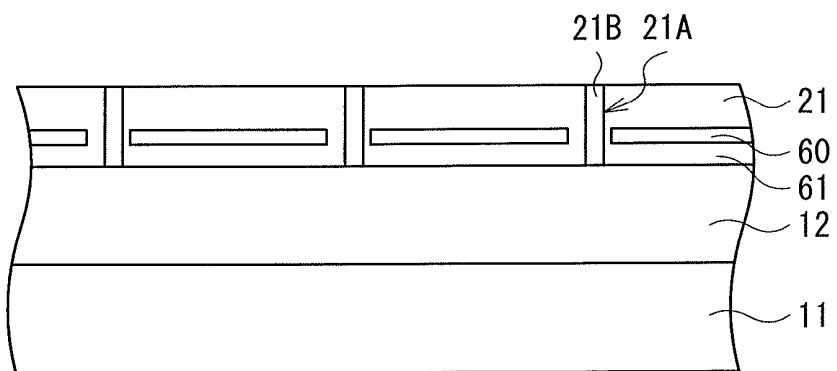
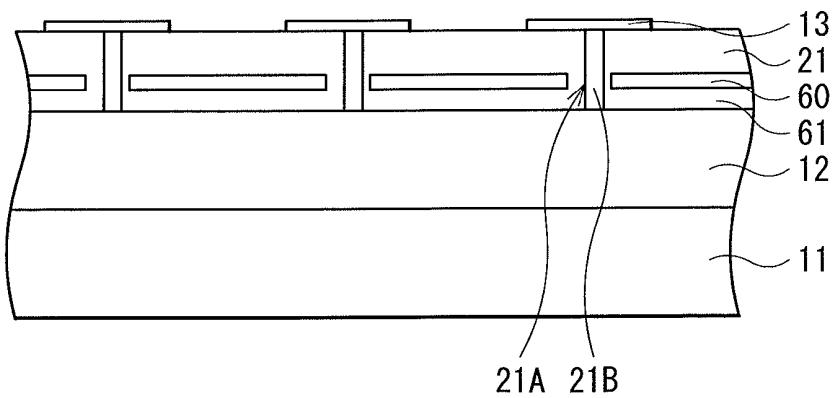
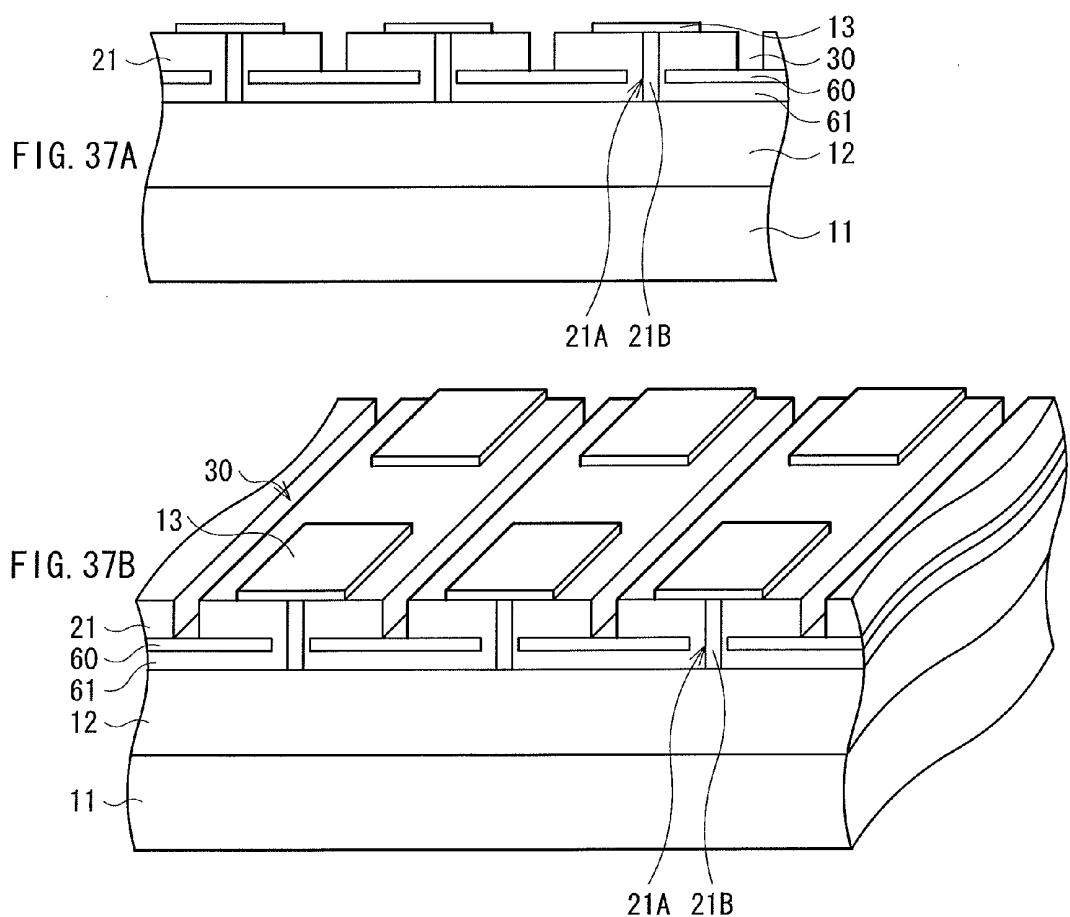


FIG. 36D





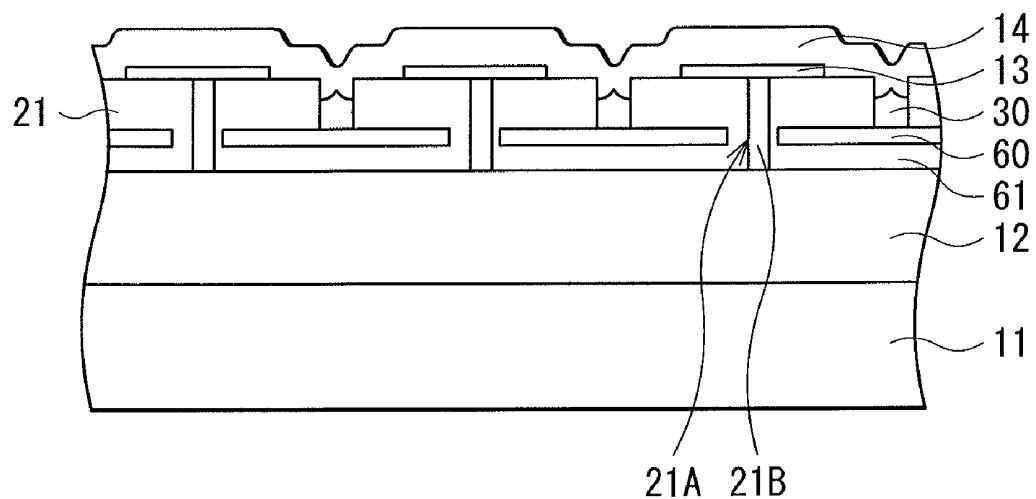


FIG. 38

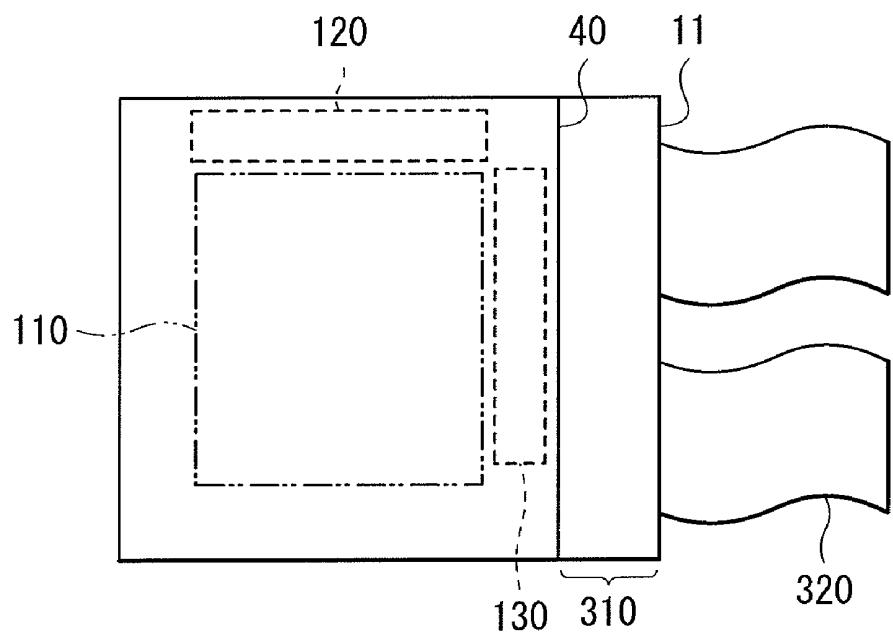


FIG. 39

FIG. 40A

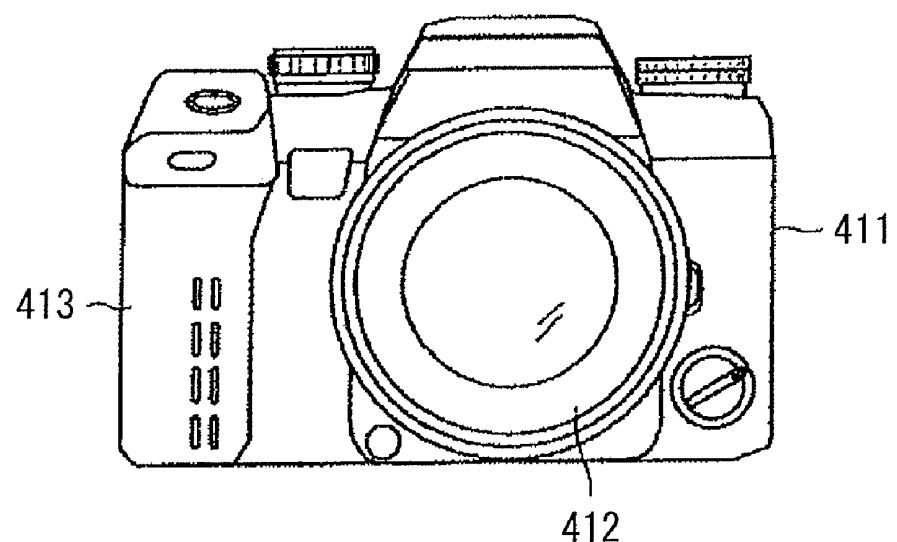
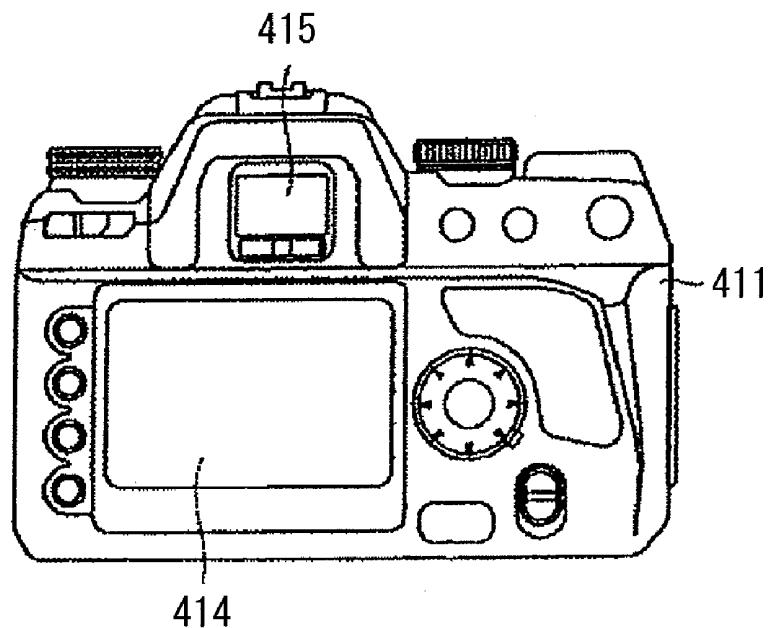


FIG. 40B



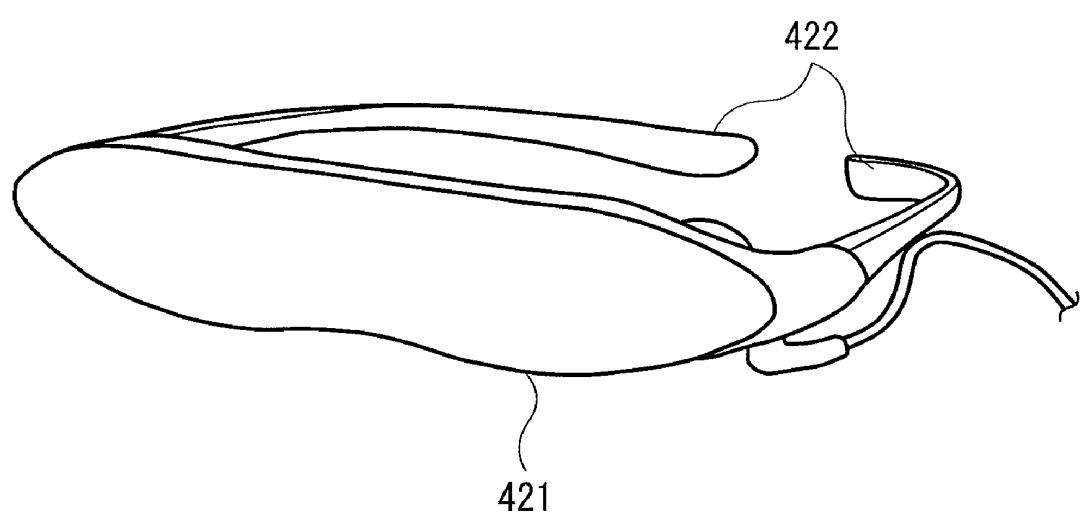


FIG. 41

DISPLAY UNIT AND METHOD OF MANUFACTURING THE SAME

CROSS REFERENCES TO RELATED APPLICATIONS

[0001] The present application claims priority to Japanese Priority Patent Application JP 2011-079619 filed in the Japan Patent Office on Mar. 31, 2011, the entire content of which is hereby incorporated by reference.

BACKGROUND

[0002] The present disclosure relates to a display unit having an organic EL (electroluminescence) device and a method of manufacturing the same.

[0003] Organic EL devices have a configuration in which a first electrode, an organic layer including a light emitting layer, and a second electrode are sequentially layered on a substrate. Examples of methods of forming the organic layer include a method of separately evaporating a red light emitting layer, a green light emitting layer, and a blue light emitting layer by using an evaporation mask and a method of layering the red light emitting layer, the green light emitting layer, and the blue light emitting layer without using the evaporation mask. In the display units expected to have high resolution and improved aperture ratio, the latter method may be mainstream in the future.

[0004] In the method of layering a plurality of light emitting layers, the organic layer is provided commonly to all organic EL devices. Therefore, drive current leakage between adjacent organic EL devices through a hole injection layer occurs. Thereby, a non-light emitting pixel slightly emits light being influenced by a light emitting pixel, which causes color mixture and lowered efficiency. To address such a disadvantage, for example, in Japanese Unexamined Patent Application Publication No. 2009-4347, in a region between organic EL devices, an inverse-tapered-shaped dividing wall is formed and a hole injection layer is subsequently formed, and thereby the hole injection layer is sectioned in parts.

SUMMARY

[0005] In the existing method described in Japanese Unexamined Patent Application Publication No. 2009-4347, after the hole injection layer is formed, heat treatment is performed to deform the dividing wall in a forward-tapered shape in order not to section a second electrode in parts in forming the second electrode on the dividing wall. However, there has been a disadvantage that in the case where heat treatment is performed in the course of an evaporation step, characteristics are deteriorated in a higher rate.

[0006] It is desirable to provide a display unit capable of suppressing drive current leakage between adjacent organic EL devices without lowering characteristics and a method of manufacturing the same.

[0007] According to an embodiment of the present disclosure, there is provided a display unit including, on a substrate a plurality of organic EL devices, and an insulating film provided in an inter-device region between the plurality of organic EL devices, the insulating film including a groove in a position between the organic EL devices adjacent to each other.

[0008] In the display unit of the embodiment of the disclosure, the insulating film is provided in the inter-device region between the plurality of organic EL devices. The insulating film has a groove in a position between the organic EL devices adjacent to each other. Therefore, the thickness inside the groove of a layer having higher conductivity out of the

organic layer such as a hole injection layer and a hole transport layer is smaller than the thickness outside the groove thereof, and resistance inside the groove thereof is increased. Therefore, drive current leakage between the organic EL devices adjacent to each other is suppressed.

[0009] According to an embodiment of the present disclosure, there is provided a method of manufacturing a display unit, the method including forming a plurality of organic EL devices on a substrate, and forming an insulating film in an inter-device region between the plurality of organic EL devices. In the forming of the insulating film, a groove is provided in a position between the organic EL devices adjacent to each other of the insulating film.

[0010] According to the display unit of the embodiment of the disclosure or the method of manufacturing a display unit of the embodiment of the disclosure, the insulating film is provided in the inter-device region between the plurality of organic EL devices. The groove is provided in the position between the organic EL devices adjacent to each other of the insulating film. Therefore, drive current leakage between the organic EL devices adjacent to each other is suppressed. Further, it is not necessary to perform the existing heat treatment, and accordingly, characteristics are allowed not to be deteriorated.

[0011] It is to be understood that both the foregoing general description and the following detailed description are exemplary, and are intended to provide further explanation of the technology as claimed.

[0012] Additional features and advantages are described herein, and will be apparent from the following Detailed Description and the figures.

BRIEF DESCRIPTION OF THE FIGURES

[0013] The accompanying drawings are included to provide a further understanding of the present disclosure, and are incorporated in and constitute a part of this specification. The drawings illustrate embodiments and, together with the specification, serve to explain the principles of the technology.

[0014] FIG. 1 is a diagram illustrating a configuration of a display unit according to a first embodiment of the present disclosure.

[0015] FIG. 2 is a diagram illustrating an example of a pixel drive circuit illustrated in FIG. 1.

[0016] FIG. 3 is a plan view illustrating a configuration of a display region illustrated in FIG. 1.

[0017] FIG. 4 is a cross-sectional view taken along a line IV-IV of FIG. 3.

[0018] FIG. 5 is a cross-sectional view of an enlarged section in the vicinity of a groove illustrated in FIG. 4.

[0019] FIG. 6 is a diagram illustrating relation between depth of the groove and sheet resistance of an inter-device region.

[0020] FIGS. 7A to 7C are diagrams illustrating measurement results of luminance of adjacent pixels in the case that the groove is provided by comparison with that in the case that the groove is not provided.

[0021] FIGS. 8A to 8C are diagrams illustrating enlarged luminance differences between the cases of the floating devices and the cases that the adjacent devices are grounded illustrated in FIGS. 7A to 7C.

[0022] FIG. 9 is a cross-sectional view illustrating a method of manufacturing the display unit illustrated in FIG. 1 in order of steps.

[0023] FIG. 10 is a cross-sectional view illustrating a step following a step in FIG. 9.

[0024] FIG. 11 is a cross-sectional view illustrating a step following the step in FIG. 10.

[0025] FIG. 12 is a cross-sectional view illustrating a step following the step in FIG. 11.

[0026] FIG. 13 is a cross-sectional view illustrating a step following the step in FIG. 12.

[0027] FIG. 14 is a cross-sectional view illustrating a step following the step in FIG. 13.

[0028] FIG. 15 is a cross-sectional view illustrating a step following the step in FIG. 14.

[0029] FIG. 16 is a cross-sectional view illustrating an enlarged section in the vicinity of a groove of a display region in a display unit according to a second embodiment of the present disclosure.

[0030] FIG. 17 is a diagram for explaining a method of manufacturing the display unit illustrated in FIG. 16.

[0031] FIG. 18 is a diagram for explaining a rotary evaporation method.

[0032] FIG. 19 is a diagram for explaining a linear evaporation method.

[0033] FIG. 20 is a view illustrating a configuration viewed in a substrate movement direction and in a direction perpendicular thereto of the linear evaporation method illustrated in FIG. 19.

[0034] FIG. 21 is a diagram illustrating a first simulation result of thickness inside the groove of a hole injection layer and a hole transport layer.

[0035] FIG. 22 is a diagram illustrating a second simulation result of the thickness inside the groove of the hole injection layer and the hole transport layer.

[0036] FIG. 23 is a diagram illustrating a third simulation result of the thickness inside the groove of the hole injection layer and the hole transport layer.

[0037] FIG. 24 is a plan view illustrating a configuration of a display region in a display unit according to a third embodiment of the present disclosure.

[0038] FIG. 25 is a diagram for explaining resistance of a second electrode and a display state in the case where a groove illustrated in FIG. 24 is provided.

[0039] FIG. 26 is a diagram for explaining resistance of the second electrode and a display state in the case where the groove illustrated in FIG. 3 is provided.

[0040] FIG. 27 is a circuit diagram illustrating current I_{ds} flowing in an organic EL device in the pixel drive circuit illustrated in FIG. 2.

[0041] FIG. 28 is a diagram for explaining simulation obtained by examining resistance of the second electrode and a drop voltage by varying distance in a column direction between the respective grooves illustrated in FIG. 24 and thickness of the second electrode.

[0042] FIG. 29 is a diagram illustrating a simulation result of resistance in the column direction and resistance in a row direction of the second electrode.

[0043] FIG. 30 is a diagram illustrating simulation result of a drop voltage of a second voltage.

[0044] FIG. 31 is a cross-sectional view illustrating a configuration of a display region in a display unit according to a fourth embodiment of the present disclosure.

[0045] FIG. 32 is a cross-sectional view illustrating an enlarged section in the vicinity of a groove illustrated in FIG. 31.

[0046] FIG. 33 is a cross-sectional view illustrating another shape of the groove illustrated in FIG. 31.

[0047] FIG. 34 is a cross-sectional view illustrating a configuration of a display region in a display unit according to a fifth embodiment of the present disclosure.

[0048] FIG. 35 is a cross-sectional view illustrating an enlarged section in the vicinity of a groove illustrated in FIG. 34.

[0049] FIGS. 36A to 36D are cross-sectional views illustrating a method of manufacturing the display unit illustrated in FIG. 34 in order of steps.

[0050] FIGS. 37A and 37B are a perspective view and a cross-sectional view illustrating a step following the step in FIG. 36D.

[0051] FIG. 38 is a cross-sectional view illustrating a step following the step in FIGS. 37A and 37B.

[0052] FIG. 39 is a plan view illustrating a schematic configuration of a module including the display unit of the foregoing embodiments.

[0053] FIGS. 40A and 40B are an elevation view and a rear view illustrating an appearance of a first application example of the display unit of the foregoing embodiments.

[0054] FIG. 41 is a perspective view illustrating an appearance of a second application example.

DETAILED DESCRIPTION

[0055] Embodiments of the present disclosure will be hereinafter described in detail with reference to the drawings. The description will be given in the following order.

1. First embodiment (example that a groove is provided in a position between adjacent organic EL devices, in an insulating film)

2. Second embodiment (example that in a step of forming an organic layer, position relation between a groove on a substrate and an evaporation source is defined)

3. Third embodiment (example that a groove is provided for each row of a plurality of organic EL devices in a column direction)

4. Fourth embodiment (example that width of a groove is changed in a two-step manner)

5. Fifth embodiment (example that a conductive film is provided on a bottom surface of a groove)

[0056] FIG. 1 illustrates a configuration of a display unit according to a first embodiment of the present disclosure. The display unit is a small high-definition organic EL display unit used for a viewfinder of a digital single-lens reflex camera, a head-mounted display, and the like. The display unit is, for example, provided with a display region 110 in which a plurality of organic EL devices 10R, 10G, and 10B described later are arranged in a matrix state on a substrate 11 made of glass or the like. A signal line drive circuit 120 and a scanning line drive circuit 130 that are drivers for displaying a video are provided on the periphery of the display region 110.

[0057] In the display region 110, a pixel drive circuit 140 is provided. FIG. 2 illustrates an example of the pixel drive circuit 140. The pixel drive circuit 140 is an active drive circuit that is formed in a layer located lower than an aforementioned first electrode 13. The pixel drive circuit 140 has, for example, a drive transistor Tr1, a writing transistor Tr2, a capacitor (retentive capacitance) Cs, and the organic EL device 10R (or 10G/10B) serially connected to the drive transistor Tr1 between a first power line (Vcc) and a second power line (GND). One electrode of the capacitor Cs is connected between the drive transistor Tr1 and the writing transistor Tr2, and the other electrode of the capacitor Cs is connected between the drive transistor Tr1 and the organic EL device 10R (or 10G/10B).

[0058] In the pixel drive circuit 140, a plurality of signal lines 120A are arranged in the column direction, and a plurality of scanning lines 130A are arranged in the row direction. Each cross section of each signal line 120A and each scanning line 130A corresponds to one of the organic EL devices 10R, 10G, and 10B (sub-pixel). Each signal line 120A is connected to the signal line drive circuit 120. An image signal is supplied to a source electrode of the writing

transistor Tr2 from the signal line drive circuit 120 through the signal line 120A. Each scanning line 130A is connected to the scanning line drive circuit 130. A scanning signal is sequentially supplied to a gate electrode of the writing transistor Tr2 from the scanning line drive circuit 130 through the scanning line 130A.

[0059] FIG. 3 illustrates a planar configuration of part of the display region 110 illustrated in FIG. 1. The plurality of organic EL devices 10R, 10G, and 10B are arranged in a matrix state on the substrate 11. The respective organic EL devices 10R, 10G, and 10B have a rectangle shape with a long side in one direction. In a row direction parallel with a short side thereof, the organic EL devices 10R, 10G, and 10B with different colors are arranged in order. In the column direction parallel with a long side thereof, the organic EL devices 10R (or 10G/10B) with the same color are arranged.

[0060] Three organic EL devices 10R, 10G, and 10B adjacent to each other compose one pixel 10. The respective organic EL devices 10R, 10G, and 10B compose one sub-pixel. Pitch (center distance) p in the row direction of the plurality of organic EL devices 10R, 10G, and 10B is, for example, equal to or smaller than 30 μm . Specifically, one pixel 10 is a square having, for example, each side of about 10 μm , and the pitch p of the plurality of organic EL devices 10R, 10G, and 10B is, for example, about 3.3 μm .

[0061] An insulating film 20 is provided in an inter-device region 10A between the plurality of organic EL devices 10R, 10G, and 10B. The insulating film 20 has a groove 30 in a position between adjacent organic EL devices 10R and 10G (or adjacent organic EL devices 10G and 10B, or adjacent organic EL devices 10B and 10R). Thereby, in the display unit, drive current leakage between adjacent organic EL devices 10R and 10G (or adjacent organic EL devices 10G and 10B, or adjacent organic EL devices 10B and 10R) is allowed to be suppressed.

[0062] The groove 30 is provided, for example, continuously from the upper end to the lower end of the display region 110 over the plurality of rows of the plurality of organic EL devices 10R, 10G, and 10B in the column direction. Thereby, if an after-mentioned second electrode 15 of the organic EL devices 10R, 10G, and 10B is sectioned in the row direction by the groove 30, the second electrode 15 is allowed to exist as a continuous common electrode in the column direction. For details of the groove 30, a description will be given later.

[0063] FIG. 4 illustrates a cross-sectional configuration taken along a line IV-IV of FIG. 3. On the substrate 11, a drive circuit layer 12 including the foregoing pixel drive circuit 140 and the like are provided. The whole surface on which the drive circuit layer 12 is provided of the substrate 11 is covered with a first insulating film 21. On the first insulating film 21, the foregoing plurality of organic EL devices 10R, 10G, and 10B are provided. In FIG. 4, only the organic EL devices 10R and 10G are illustrated. In the inter-device region 10A between the plurality of organic EL devices 10R, 10G, and 10B, a second insulating film 22 is provided on the first insulating film 21. The first insulating film 21 and the second insulating film 22 compose the insulating film 20 in FIG. 3. The groove 30 penetrates through the second insulating film 22, and is provided in part of the first insulating film 21 in a thickness direction.

[0064] The first insulating film 21 is intended to planarize the surface of the substrate 11 on which the pixel circuit layer 12 is formed. The first insulating film 21 has a thickness of, for example, from 100 nm to 1000 nm both inclusive, and is made of silicon oxide nitride (SiON) or silicon oxide (SiO₂ or SiO). In the case where component material of the first insulating

film 21 is a silicon-based material such as SiON, SiO₂, and SiO, the groove 30 that is deep in the thickness direction of the first insulating film 21 is allowed to be formed easily by etching. The first insulating film 21 is provided with a contact hole 21A for connecting to the drive transistor Tr1 of the pixel drive circuit 140 of the drive circuit layer 12. The contact hole 21A is provided with a plug 21B made of conductive metal.

[0065] The second insulating film 22 is intended to secure insulation between the first electrode 13 and the second electrode 15, and to obtain a desired shape of the light emitting region accurately. The second insulating film 22 covers not only the inter-device region 10A but also the top face end of the first electrode 13, and has an aperture 22A corresponding to the light emitting region of the first electrode 13. The second insulating film 22 has a thickness of, for example, from 100 nm to 200 nm both inclusive, and is made of SiON. In the case where the component material of the second insulating film 22 is a silicon-based material such as SiON, the groove 30 that is deep in the thickness direction of the second insulating film 22 is allowed to be formed easily by etching.

[0066] The organic EL devices 10R, 10G, and 10B are provided on the first insulating film 21. In the organic EL devices 10R, 10G, and 10B, the first electrode 13, an organic layer 14 including a light emitting layer, and the second electrode 15 are layered in this order of closeness to the substrate 11. In FIG. 4, though only the organic EL devices 10R and 10G are illustrated, the organic EL device 10B has the same configuration.

[0067] The organic EL devices 10R, 10G, and 10B are covered with a protective film 16. Further, a sealing substrate 40 made of glass or the like is bonded to the whole surface of the protective film 16 with an adhesive layer 17 in between, and thereby the organic EL devices 10R, 10G, and 10B are sealed.

[0068] The first electrode 13 is provided for each of the plurality of organic EL devices 10R, 10G, and 10B. The first electrode 13 has a thickness of, for example, about 100 nm, is made of aluminum (Al) as a high reflective material or an alloy containing aluminum (Al), and extracts light generated in the light emitting layer from the second electrode 15 side (top emission). The thickness of the first electrode 13 is preferably a value with which the light generated in the light emitting layer is not transmitted therethrough and light extraction efficiency is allowed to be retained, for example, in the range from 30 nm to 200 nm both inclusive. Examples of component material of the first electrode 13 include a reflecting electrode made of aluminum (Al), an alloy thereof, and a simple substance or an alloy of metal elements such as gold (Au), platinum (Pt), nickel (Ni), chromium (Cr), copper (Cu), tungsten (W), molybdenum (Mo), titanium (Ti), tantalum (Ta), and silver (Ag).

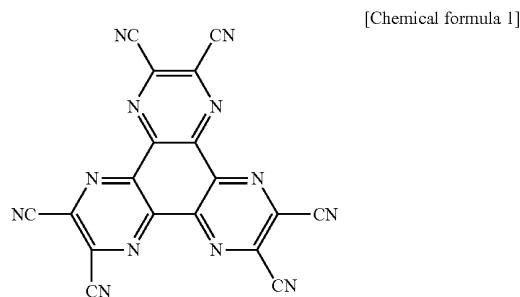
[0069] Further, the first electrode 13 may have a contact layer (not illustrated) being about 20 nm thick that is made of titanium (Ti), tungsten (W), copper (Cu), tantalum (Ta), molybdenum (Mo), or the like as a base of the foregoing reflecting electrode. The contact layer also has a function as a reflective supplementary layer to retain high reflectance even if the thickness of the first electrode 13 is decreased. In the case where the contact layer is provided, it is enough that the thickness of the first electrode 13 is equal to or larger than 15 nm.

[0070] Further, the first electrode 13 may have a three-layer laminated structure composed of a titanium layer as the contact layer or the reflective supplementary layer, the foregoing reflecting electrode made of aluminum, an alloy thereof, or the like, and a titanium layer or a tantalum layer. Otherwise,

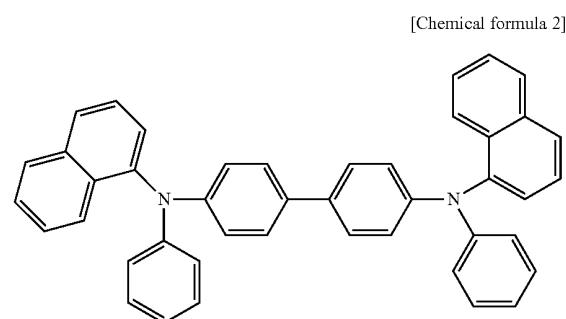
the first electrode **13** may be formed of a composite film composed of the foregoing reflecting electrode and a transparent electrode made of ITO (Indium Tin Oxide), IZO (registered trademark) (indium zinc composite oxide), SnO₂, or the like.

[0071] The organic layer **14** is provided on the first electrode **13** and the second insulating film **22** commonly to the plurality of organic EL devices **10R**, **10G**, and **10B**. The organic layer **14** has, for example, a configuration in which a hole injection layer **14A**, a hole transport layer **14B**, a light emitting layer **14C**, and an electron transport layer **14D** are layered in order of closeness to the first electrode **13** as illustrated in FIG. 5.

[0072] The hole injection layer **14A** is intended to improve hole injection efficiency, and is a buffer layer to prevent leakage. The hole injection layer **14A** has a thickness of, for example, from 2 nm to 10 nm both inclusive, and is composed of hexatrilazatriphenylene shown in Chemical formula 1.



[0073] The hole transport layer **14B** is intended to improve hole injection efficiency to the light emitting layer **14C**. The hole transport layer **14B** has a thickness of, for example, 30 nm, and is composed of a material shown in Chemical formula 2.

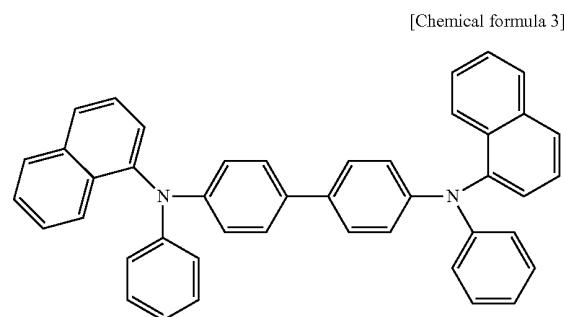


[0074] The light emitting layer **14C** is a white light emitting layer in which a red light emitting layer being 10 nm thick (not illustrated), a light emitting separation layer being 10 nm thick (not illustrated), a blue light emitting layer being 10 nm thick (not illustrated), and a green light emitting layer being 10 nm thick (not illustrated) are layered in order of closeness to the first electrode **13**. The red light emitting layer generates red light by recombination of part of holes injected from the first electrode **13** through the hole injection layer **14A** and the hole transport layer **14B** and part of electrons injected from the second electrode **15** through the electron transport layer

14D by being applied with an electric field. The light emitting separation layer is intended to decrease electron supply amount to the red light emitting layer. The blue light emitting layer generates blue light by recombination of part of holes injected from the first electrode **13** through the hole injection layer **14A**, the hole transport layer **14B**, and the light emitting separation layer and part of electrons injected from the second electrode **15** through the electron transport layer **14D** by being applied with an electric field. The green light emitting layer generates green light by recombination of part of holes injected from the first electrode **13** through the hole injection layer **14A**, the hole transport layer **14B**, and the light emitting separation layer and part of electrons injected from the second electrode **15** through the electron transport layer **14D** by being applied with an electric field. The red light emitting layer, the green light emitting layer, and the blue light emitting layer emit light in a region corresponding to the aperture **22A** of the second insulating film **22**.

[0075] The red light emitting layer contains one or more of a red light emitting material, a hole transport material, an electron transport material, and a both-electric-charge transport material. The red light emitting material may be a fluorescent material or a phosphorescent material. Specifically, the red light emitting layer has a thickness of, for example, about 5 nm, and is composed of a mixture obtained by mixing 30 wt % of 2,6-bis[(4'-methoxydiphenylamino)styryl]-1,5-dicyanonaphthalene (BSN) in 4,4-bis(2,2-diphenylvinyl)biphenyl (DPVBi).

[0076] The light emitting separation layer is composed of a material shown in Chemical formula 3, for example.



[0077] The green light emitting layer contains, for example, one or more of a green light emitting material, a hole transport material, an electron transport material, and a both-electric-charge transport material. The green light emitting material may be a fluorescent material or a phosphorescent material. Specifically, the green light emitting layer has a thickness of, for example, about 10 nm, and is composed of a mixture obtained by mixing 5 wt % of coumarin 6 in DPVBi.

[0078] The blue light emitting layer contains, for example, one or more of a blue light emitting material, a hole transport material, an electron transport material, and a both-electric-charge transport material. The blue light emitting material may be a fluorescent material or a phosphorescent material. Specifically, the blue light emitting layer has a thickness of, for example, about 30 nm, and is composed of a mixture obtained by mixing 2.5 wt % of 4,4'-bis [2-{4-(N,N-diphenylamino)phenyl}vinyl]biphenyl (DPAVBi) in DPVBi.

[0079] The electron transport layer 14D is intended to improve efficiency to inject electrons into the light emitting layer 14C. The electron transport layer 14D has a thickness of, for example, about 20 nm, and is composed of 8-hydroxyquinolinealuminum (Alq3).

[0080] The second electrode 15 illustrated in FIG. 4 is provided on the organic layer 14 commonly to the plurality of organic EL devices 10R, 10G, and 10B. The second electrode has a configuration in which, for example, a first layer composed of lithium fluoride (Lif) being about 0.3 nm thick, a second layer composed of calcium (Ca) being about 3 nm thick, and a third layer composed of Mg—Ag alloy being about 5 nm thick are layered in order of closeness to the first electrode 13. The second electrode 15 is connected to an auxiliary wiring (not illustrated) in a region outside the display region 110 for the following reason. That is, since the organic layer 14 is provided commonly to the plurality of organic EL devices 10R, 10G, and 10B, connection between the second electrode 15 and the auxiliary wiring is not allowed to be made for each of the organic EL devices 10R, 10G, and 10B.

[0081] The protective film 16 illustrated in FIG. 4 has a thickness of, for example, from 0.5 μm to 10 μm both inclusive, and is composed of silicon nitride (SiN). The adhesive layer 17 illustrated in FIG. 4 is composed of, for example, an ultraviolet cured resin or a thermoset resin.

[0082] The sealing substrate 40 illustrated in FIG. 4 is located on the second electrode 15 side of the organic EL devices 10R, 10G, and 10B, and seals the organic EL devices 10R, 10G, and 10B together with the adhesive layer 17. The sealing substrate 40 is made of, for example, a material such as glass transparent to light generated in the organic EL devices 10R, 10G, and 10B.

[0083] The sealing substrate 40 is provided with, for example, a color filter 41 and a light shielding film 42 as a black matrix. The color filter 41 is intended to extract white light generated in the organic EL devices 10R, 10G, and 10B as red, green, or blue light, and has a red filter 41R, a green filter 41G, and a blue filter (not illustrated). The red filter 41R, the green filter 41G, and the blue filter (not illustrated) are sequentially arranged correspondingly to the organic EL devices 10R, 10G, and 10B. The red filter 41R, the green filter 41G, and the blue filter (not illustrated) are respectively made of a resin mixed with a pigment. Adjustment is made by selecting a pigment so that light transmittance in the intended red, green, or blue wavelength region is high, and light transmittance in the other wavelength regions is low.

[0084] The light shielding film 42 is intended to absorb outside light reflected by the organic EL devices 10R, 10G, and 10B and wiring therebetween, and to improve contrast. The light shielding film 42 is composed of, for example, a black resin film having an optical density equal to or larger than 1 in which a black colorant is mixed, or a thin film filter by using thin film interference. Of the foregoing, the light shielding film 42 is preferably composed of the black resin film, since thereby the film is allowed to be formed inexpensively and easily. The thin film filter is obtained by layering one or more thin films composed of metal, metal nitride, or metal oxide, and is intended to attenuate light by using thin film interference. Specific examples of the thin film filter include a filter in which chromium and chromium oxide (III) (Cr_2O_3) are alternately layered.

[0085] The groove 30 illustrated in FIG. 3 and FIG. 4 is intended to suppress drive current leakage in the inter-device region 10A as described above. That is, the organic layer 14 is provided as a layer common to the plurality of organic EL devices 10R, 10G, and 10B. Further, the hole injection layer 14A and the hole transport layer 14B illustrated in FIG. 5 have relatively high conductivity out of the organic layer 14. Therefore, the hole injection layer 14A and the hole transport layer 14B easily form a leakage path between the first electrodes 13 of adjacent organic EL devices 10R, 10G, and 10B. Therefore, for example, if the organic EL device 10R emits light, the adjacent organic EL device 10G emits light, and accordingly chromaticity of simple red color is shifted.

[0086] Influence of such a leakage current in the inter-device region 10A is more significant as size of the organic EL devices 10R, 10G, and 10B is smaller. For example, discussion will be given of leakage current I_L flowing in square region 10A1 sandwiched between a long side of the organic EL device 10R and a long side of the adjacent organic EL device 10G in FIG. 3. Resistance R in the square region 10A1 is determined by a ratio between distance between the organic EL devices 10R and 10G and length of the long sides of the organic EL devices 10R and 10G (sheet resistance), and is constant without relation to the size of the organic EL devices 10R, 10G, and 10B. Further, JV characteristics (J: current density) of the organic EL devices 10R, 10G, and 10B are constant without relation to the size of the organic EL devices 10R, 10G, and 10B. That is, voltage V necessary for 100 nit light emission on the whole surface is constant without relation to the size of the organic EL devices 10R, 10G, and 10B. Therefore, the leakage current I_L ($=V/R$) is constant without relation to the size of the organic EL devices 10R, 10G, and 10B. Meanwhile, drive current I_0 used for light emission varies one digit or more according to the size of the organic EL devices 10R, 10G, and 10B. Therefore, the ratio of the leakage current I_L with respect to the drive current I_0 used for light emission becomes larger as the size of the organic EL devices 10R, 10G, and 10B gets smaller, resulting in larger influence on image quality.

[0087] In this case, by providing the groove 30 in the inter-device region 10A, as illustrated in FIG. 5, thickness t_1 inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B is allowed to be decreased down to about one tenth of thickness t_2 outside the groove 30 thereof. Therefore, it is possible to increase resistance inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B and suppress drive current leakage in the inter-device region 10A. Thereby, the ratio of the leakage current I_L with respect to the drive current I_0 used for light emission becomes equal to or smaller than several %, and influence of leakage current on image quality is allowed to be decreased down to almost invisible level.

[0088] Width w of the groove 30 is preferably equal to or smaller than the total film thickness of the organic layer 14, and specifically, is preferably from 10 nm to 150 nm both inclusive. Thereby, as illustrated in FIG. 5, the light emitting layer 14C is allowed to be continuous over the groove 30. As a result, the electron transport layer 14D and the second electrode 15 are allowed to be continuous without being sectioned in parts over the groove 30. Therefore, it is allowed to avoid an event that the second electrode 15 is cut by the groove 30, and to provide the groove 30 not only in the column direction but also in the row direction in a lattice pattern.

[0089] FIG. 6 illustrates a result obtained by examining relation between depth d of the groove 30 and sheet resistance in the inter-device region 10A including the groove 30. FIG. 6 illustrates a measurement result of a case in which the pitch p of the plurality of organic EL devices 10R, 10G, and 10B is about 3.3 μ m. As can be seen from Table 6, as the depth d of the groove 30 is deeper, a leakage current is allowed to be decreased more. For example, for suppressing light emission of adjacent devices in the case where the pitch p is about 3.3 μ m almost entirely, sheet resistance equivalent value of about $2*12\Omega/\square$ may be necessitated, and the depth d equal to or larger than 500 nm may be necessitated.

[0090] A description will be hereinafter given of relation between the pitch of the organic EL devices 10R, 10G, and 10B and a necessary resistance value.

[0091] Discussion will be given of two similar figure pixels having different pitches, where respective pitches are p and p'; respective light emitting areas are S and S'; respective drive currents are I_0 and I_0' ; respective leakage currents are I_L and I_L' ; and respective necessary resistance values are R and R'.

[0092] Respective current densities at certain luminance are identical. Therefore, the following expression is established:

$$I'_0 = (S'/S) I_0$$

$$= (p'/p)^2 I_0$$

[0093] Where the ratio of the leakage current I_L to the necessary drive current I_0 is r and r', the following expressions are established:

$$I_L = r I_0$$

$$I_L' = r' I_0$$

[0094] where $r' = r$

[0095] Further, respective drive voltages are identical, which is V. Therefore, the following expression is established:

$$R' = V/I'_L$$

$$= (r I_0 R)/(r I'_0)$$

$$= (I_0 R)/((p'/p)^2 I_0)$$

[0096] Accordingly, the following expression is established:

$$R'/R = (p/p')^2$$

[0097] FIGS. 7A to 7C illustrate results obtained by making one device emit light and measuring ratio of luminance of a device adjacent to the foregoing one device with respect to luminance of the foregoing one device. The device practically emitting light is located in the vicinity of from 0.5 to 1 both inclusive of the horizontal axis, and the device adjacent thereto is located in the vicinity of from 3.5 to 4 both inclusive of the horizontal axis. FIG. 7A illustrates a measurement result of Comparative example 1 in which a groove was not provided, FIG. 7B illustrates a measurement result of Example 1-1 in which the depth d of the groove 30 was 300 nm, and FIG. 7C illustrates a measurement result of Example 1-2 in which the depth d of the groove 30 was 700 nm. Further, in FIGS. 7A to 7C, full lines indicate luminance in a case that

the adjacent device is a floating device, and dashed lines indicate luminance in a case that the adjacent device is grounded (0 V). Difference between the two values of luminance represents a leakage current. FIGS. 8A to 8C indicate the measurement results by enlarging the foregoing difference.

[0098] As can be seen from FIGS. 7A to 7C and FIGS. 8A to 8C, in Examples 1-1 and 1-2 in which the groove 30 was provided, the luminance ratios of the adjacent device were lower than that of Comparative example 1 in which a groove was not provided. That is, it is found that, in the case where the groove 30 is provided in the inter-pixel region 10A, drive current leakage between adjacent organic EL devices 10R, 10G, and 10B is allowed to be suppressed.

[0099] Further, comparing Example 1-1 to Example 1-2, as the depth d of the groove 30 was deeper, the luminance ratio of the adjacent device was lower for the following possible reason. That is, the thickness t1 inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B is extremely thin, and resistance of the section with such a thin thickness t1 is increased. Therefore, as the depth d of the groove 30 is deeper, resistance of the entire inter-device region 10A is increased. That is, it is found that, as the depth d of the groove 30 is deeper, drive current leakage between the adjacent organic EL devices 10R, 10G, and 10B is allowed to be suppressed.

[0100] The display unit is allowed to be manufactured, for example, as follows.

[0101] FIG. 9 to FIG. 15 illustrate a method of manufacturing the display unit in order of steps. First, as illustrated in FIG. 9, the drive circuit layer 12 including the pixel drive circuit 140 is formed on the substrate 11 made of the foregoing material. Next, for example, an SiON film, an SiO_2 film, or an SiO film having, for example, a thickness from 100 nm to 1000 nm both inclusive is formed by, for example, a plasma CVD (Plasma-Enhanced Chemical Vapor Deposition) method. Subsequently, the SiON film, the SiO_2 film, or the SiO film is formed in a determined shape by, for example, a photolithography method and a dry etching method. Again, as illustrated in FIG. 9, the first insulating film 21 having the contact hole 21A is formed. After that, again, as illustrated in FIG. 9, the plug 21B made of conductive metal is buried in the contact hole 21A of the first insulating film 21.

[0102] Subsequently, for example, a titanium film and an aluminum alloy film (not illustrated) are formed on the first insulating film 21 by, for example, a sputtering method. After that, the titanium film and the aluminum alloy film are formed in a determined shape by, for example, a photolithography method and a dry etching method to form the first electrode 13 for each of the plurality of organic EL devices 10R, 10G, and 10B as illustrated in FIG. 10.

[0103] After the first electrode 13 is formed, an SiON film having, for example, a thickness from 10 nm to 200 nm both inclusive is formed on the first electrode 13 and the first insulating film 21 by a PECVD method. The SiON film is formed in a determined shape by, for example, a photolithography method and a dry etching to form the second insulating film 22 having the aperture 22A as illustrated in FIG. 11.

[0104] After the second insulating film 22 is formed, as illustrated in FIG. 12, the groove 30 is provided in the second insulating film 22 and the first insulating film 21 by, for example, a photolithography method and dry etching. In the first insulating film 21, a stopper layer (not illustrated) for etching the groove 30 is allowed to be provided.

[0105] After the groove **30** is provided, as illustrated in FIG. 13, the hole injection layer **14A** and the hole transport layer **14B** of the organic layer **14** are formed on the first electrode **13** and the second insulating film **22** by, for example, an evaporation method. At this time, the thickness **t1** inside the groove **30** of the hole injection layer **14A** and the hole transport layer **14B** is decreased down to about one tenth of the thickness **t2** outside the groove **30** thereof. Therefore, resistance inside the groove **30** of the hole injection layer **14A** and the hole transport layer **14B** is increased, and drive current leakage in the inter-device region **10A** is suppressed.

[0106] Subsequently, as illustrated in FIG. 14 and FIG. 15, the light emitting layer **14C** and the electron transport layer **14D** of the organic layer **14** are formed by, for example, an evaporation method. Subsequently, the second electrode **15** is formed by, for example, a vacuum evaporation method. At this time, since the width **w** of the groove **30** is, for example, equal to or smaller than the total film thickness of the organic layer **14**, specifically, is from 10 nm to 150 nm both inclusive, the light emitting layer **14C** is allowed to be continuous over the groove **30**. As a result, the electron transport layer **14D** and the second electrode **15** are allowed to be continuous without being sectioned over the groove **30**, and it becomes possible to avoid a state that the second electrode **15** is sectioned in parts by the groove **30**. Accordingly, the organic EL devices **10R**, **10G**, and **10B** illustrated in FIG. 3 and FIG. 4 are formed.

[0107] Subsequently, as illustrated in FIG. 4, the protective film **16** made of the foregoing material is formed on the organic EL devices **10R**, **10G**, and **10B** by, for example, a CVD method or a sputtering method.

[0108] Further, as illustrated in FIG. 4 again, for example, the light shielding film **42** made of the foregoing material is formed on the sealing substrate **40** made of the foregoing material. Subsequently, the sealing substrate **40** is coated with a material of the red filter **41R** by spin coating or the like. The resultant is patterned by photolithography technology, burned, and thereby the red filter **41R** is formed. Subsequently, the blue filter (not illustrated) and the green filter **41G** are sequentially formed in the same manner as in the red filter **41R**.

[0109] After that, as illustrated in FIG. 4 again, the adhesive layer **17** is formed on the protective film **16**, and the sealing substrate **40** is bonded with the protective layer **16** with the adhesive layer **17** in between. Accordingly, the display unit illustrated in FIG. 1 to FIG. 4 is completed.

[0110] In the display unit, the scanning signal is supplied to each pixel through the gate electrode of the writing transistor **Tr2** from the scanning line drive circuit **130**, and the image signal from the signal line drive circuit **120** is retained in the retentive capacitance **Cs** through the writing transistor **Tr2**. That is, the drive transistor **Tr1** is on/off-controlled according to the signal retained in the retentive capacitance **Cs**, and thereby drive current **Ids** is injected into the respective organic EL devices **10R**, **10G**, and **10B**, electron-hole recombination is generated, and thereby light is emitted. The light is transmitted through the second electrode **15**, the protective film **16**, the adhesive layer **17**, the color filter **41**, and the sealing substrate **40** (top emission), and is extracted.

[0111] In this case, the insulating film **20** (the first insulating film **21** and the second insulating film **22**) is provided in the inter-device region **10A** between the plurality of organic EL devices **10R**, **10G**, and **10B**. The insulating film **20** has the groove **30** in a position between adjacent organic EL devices

10R and **10G** (adjacent organic EL devices **10G** and **10B**, or adjacent organic EL devices **10B** and **10R**). Therefore, the thickness **t1** inside the groove **30** of a layer having higher conductivity out of the organic layer **14** such as the hole injection layer **14A** and the hole transport layer **14B** becomes smaller than the thickness **t2** outside the groove **30** thereof, and resistance inside the groove **30** is increased. Therefore, drive current leakage between adjacent organic EL devices **10R**, **10G**, and **10B** is suppressed.

[0112] As described above, in the display unit of this embodiment, the insulating film **20** (the first insulating film **21** and the second insulating film **22**) is provided in the inter-device region **10A** between the plurality of organic EL devices **10R**, **10G**, and **10B**. The groove **30** is provided in a position between adjacent organic EL devices **10R** and **10G** (or adjacent organic EL devices **10G** and **10B**, or adjacent organic EL devices **10B** and **10R**) in the insulating film **20**. Therefore, drive current leakage between adjacent organic EL devices **10R**, **10G**, and **10B** is allowed to be suppressed. In addition, differently from the existing display unit, it is not necessary to submit the inverse-tapered-shaped dividing wall to heat treatment to change the shape thereof to forward-tapered shape in the course of an evaporation step, and accordingly characteristics lowering is avoided.

[0113] In the foregoing embodiment, the description has been given of the case in which the first insulating film **21** and the second insulating film **22** are layered as the insulating film **20**. However, it is possible that the second insulating film **22** is omitted and only the first insulating film **21** is provided as the insulating film **20**.

Second Embodiment

[0114] FIG. 16 illustrates an enlarged cross-sectional configuration of a section in the vicinity of the groove **30** of the display region **110** in the display unit according to a second embodiment of the present disclosure. In this embodiment, the thickness **t1** inside the groove **30** of the hole injection layer **14A** and the hole transport layer **14B** gets smaller as its position in the groove **30** is deeper. Thereby, in this embodiment, resistance of the hole injection layer **14A** and the hole transport layer **14B** is allowed to be more increased, and drive current leakage between adjacent organic EL devices **10R**, **10G**, and **10B** is allowed to be more suppressed. Except for such a point, the display unit of this embodiment has the same configuration, the same function, and the same effect as those of the foregoing first embodiment.

[0115] The display unit is allowed to be manufactured, for example, as follows. For manufacturing steps similar to those of the first embodiment, a description will be given with reference to FIG. 9 to FIG. 15.

[0116] First, as illustrated in FIG. 9, the drive circuit layer **12** including the pixel drive circuit **140**, and the first insulating film **21** are formed on the substrate **11** as in the first embodiment.

[0117] Next, as illustrated in FIG. 10, the first electrode **13** is formed for each of the plurality of organic EL devices **10R**, **10G**, and **10B** on the first insulating film **21** as in the first embodiment.

[0118] Subsequently, as illustrated in FIG. 11, the second insulating film **22** is formed as in the first embodiment.

[0119] After that, as illustrated in FIG. 12, the groove **30** is provided in the second insulating film **22** and the first insulating film **21** as in the first embodiment.

[0120] After the groove 30 is provided, as illustrated in FIG. 13 to FIG. 15, the hole injection layer 14A, the hole transport layer 14B, the light emitting layer 14C, and the electron transport layer 14D of the organic layer 14 are formed over the first electrode 13 and the second insulating film 22 by, for example, an evaporation method.

[0121] In the step of forming the organic layer 14, as illustrated in FIG. 17, position relation between the groove 30 on the substrate 11 and an evaporation source 50 preferably satisfies Mathematical expression 1. Thereby, as illustrated in FIG. 16, it becomes possible that the thickness t_1 inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B gets smaller as its position in the groove 30 is deeper. It is to be noted that in this case, the substrate 11 does not mean the substrate 11 itself, but means the surface of the substrate 11 at the time of evaporation, specifically, the surface of the second insulating film 22.

$X/Y > w/d$

[Mathematical expression 1]

[0122] In the expression, X represents offset distance from the entrance edge of the groove 30 to the evaporation source 50, Y represents distance between the substrate 11 and the evaporation source 50, w represents width of the groove, and d represents depth of the groove 30.

[0123] Mathematical expression 1 expresses $\tan \theta > \tan \alpha$, that is, $\theta > \alpha$ in FIG. 17, where θ is an angle made by a vertical line drawn from the evaporation source 50 to the substrate 11 and a straight line from the evaporation source 50 to the entrance of the groove 30, and α is an angle made by a wall surface of the groove 30 and a straight line from the entrance of the groove 30 to the bottom surface thereof.

[0124] As an evaporation method, for example, a rotary evaporation method or a linear evaporation method may be used. The rotary evaporation method is a method in which the cell type evaporation source 50 is used and film formation is performed while the substrate 11 is rotated above the evaporation source 50. At this time, the evaporation source 50 is arranged so that the position relation between the groove 30 on the substrate 11 and the evaporation source 50 satisfies Mathematical expression 1. Further, as long as Mathematical expression 1 is satisfied during part or all of a time period in which the substrate 11 is rotated once, the foregoing effect of decreasing film thickness of the hole injection layer 14A and the hole transport layer 14B is allowed to be obtained.

[0125] In the rotary evaporation method, a material is put in a crucible (not illustrated) of the evaporation source, and the material is evaporated by heating the crucible by a heater. As a material of the crucible, ceramics such as PBN and alumina, Ta, and the like are desirable. Evaporated molecules linearly fly according to directivity of the evaporation source 50 (n value), and are deposited on the substrate 11. In this case, the n value represents ratio (A/A_0) of evaporation density A in a given position with respect to evaporation density A_0 at the center, and is a value obtained by approximation with $\cos n\theta$.

[0126] For example, as illustrated in FIG. 19 and FIG. 20, the linear evaporation method is a method in which a long evaporation source 50 is used, and film formation is made while the evaporation source 50 and the substrate 11 as an evaporation target are relatively moved in direction of arrow A11. In the line-shaped evaporation source 50, a plurality of apertures 51 are provided along the longitudinal direction, and an evaporation material is ejected from each apertures 51. Film formation width 53 in the carrying direction is determined by a limitation plate 52 provided in the substrate move-

ment direction A11. At this time, the evaporation source 50 is arranged so that the position relation between the groove 30 on the substrate 11 and the evaporation source 50 satisfies Mathematical expression 1. Further, as long as Mathematical expression 1 is satisfied in part or all of a time period in which the substrate 11 passes over the evaporation source 50, the foregoing effect of decreasing film thickness of the hole injection layer 14A and the hole transport layer 14B is allowed to be obtained.

[0127] Further, in the step of forming the hole injection layer 14A or the hole transport layer 14B out of the organic layer 14, Mathematical expression 1 is preferably satisfied for the following reason. Out of the organic layer 14, the hole injection layer 14A and the hole transport layer 14B have comparatively high conductivity, and a leakage path is easily formed between the adjacent organic EL devices 10R, 10G, and 10B.

[0128] After the organic layer 14 is formed, as illustrated in FIG. 14 and FIG. 15, the second electrode 15 is formed as in the first embodiment. Thereby, the organic EL devices 10R, 10G, and 10B as illustrated in FIG. 3 and FIG. 4 are formed.

[0129] Subsequently, as illustrated in FIG. 4, the protective film 16 and the adhesive layer 17 made of the foregoing materials are formed on the organic EL devices 10R, 10G, and 10B as in the first embodiment. The resultant is bonded with the sealing substrate 40 provided with the color filter 41 and the light shielding film 42. Accordingly, the display unit illustrated in FIG. 1 to FIG. 4 is completed.

[0130] FIG. 21 is a first simulation result of thickness distribution inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B. As simulation conditions, a rotary evaporation method (see FIG. 18) in which the width w of the groove 30 is 0.4 μm , the depth d is 3.0 μm , the offset distance X from the entrance edge of the groove 30 to the evaporation source 50 is 200 mm, the distance Y between the substrate 11 and the evaporation source 50 is 600 mm, and the directivity n value of the evaporation source 50 is 2 is assumed. The thickness (target thickness) outside the groove 30 of the hole injection layer 14A and the hole transport layer 14B is 10 nm.

[0131] As can be seen from FIG. 21, the position relation between the groove 30 on the substrate 11 and the evaporation source 50 satisfies Mathematical expression 1, and the thickness t_1 inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B is constant until the depth d_1 of the groove 30 reaches 1.2 μm . In the case where the depth d_1 of the groove 30 is larger than 1.2 μm (section surrounded by a dashed line in FIG. 21), the thickness t_1 inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B becomes smaller as the depth of the groove 30 becomes larger (section surrounded by a dashed line in FIG. 21).

[0132] FIG. 22 is a second simulation result of thickness distribution inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B. As simulation conditions, a linear evaporation (see FIG. 19 and FIG. 20) in which the width w of the groove 30 is 0.6 μm , the depth d is 2.0 μm , the film formation width 53 in the carrying direction is 200 mm, the distance Y between the substrate 11 and the evaporation source 50 is 100 mm, and the directivity n value of the evaporation source 50 is 10 is assumed. The thickness (target thickness) outside the groove 30 of the hole injection layer 14A and the hole transport layer 14B is 10 nm.

[0133] As can be seen from FIG. 22, the position relation between the groove 30 on the substrate 11 and the evaporation source 50 satisfies Mathematical expression 1, and the thickness t_1 inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B is constant until the depth d_1 of the groove 30 reaches 0.6 μm . In the case where the depth d_1 of the groove 30 is larger than 0.6 μm (section surrounded by a dashed line in FIG. 22), the thickness t_1 inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B becomes smaller as the depth of the groove 30 becomes larger.

[0134] FIG. 23 is a third simulation result of thickness distribution inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B. As simulation conditions, rotary evaporation (see FIG. 18) in which the width w of the groove 30 is 0.6 μm , the depth d is 1.5 μm , the offset distance X from the entrance edge of the groove 30 to the evaporation source 50 is 150 mm, the distance Y between the substrate 11 and the evaporation source 50 is 500 mm, and the directivity n value of the evaporation source 50 is 2 is assumed. The thickness (target thickness) outside the groove 30 of the hole injection layer 14A and the hole transport layer 14B is 10 nm.

[0135] As can be seen from FIG. 23, the position relation between the groove 30 on the substrate 11 and the evaporation source 50 does not satisfy Mathematical expression 1, and the thickness t_1 inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B is constant even after the depth of the groove 30 becomes large. However, the thickness t_1 inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B is decreased down to about one tenth of the thickness t_2 outside the groove 30 thereof, which means the effect of decreasing film thickness by the groove 30 is obtained.

[0136] That is, it is found that, in the case where Mathematical expression 1 is satisfied in the step of forming the organic layer 14, the thickness t_1 inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B is allowed to be more decreased, the resistance is allowed to be more increased, and drive current leakage between the adjacent organic EL devices 10R, 10G, and 10B is allowed to be more suppressed.

Third Embodiment

[0137] FIG. 24 illustrates a planar configuration of part of a display region in a display unit according to a third embodiment of the present disclosure. In this embodiment, the groove 30 is provided for each row of the plurality of organic EL devices 10R, 10G, and 10B in the column direction. Thereby, voltage drop of the second electrode 15 is suppressed, and image quality is improved. Except for such a point, the display unit of this embodiment has the same configuration, the same function, and the same effect as those of the foregoing first or second embodiment, and is allowed to be similarly manufactured.

[0138] For example, each groove 30 with almost the same length as that of the long side of the organic EL devices 10R, 10G, and 10B is provided in a region between columns of the adjacent organic EL devices 10R, 10G, and 10B. Distance L is provided between the grooves 30 in the column direction.

[0139] FIG. 25 illustrates an equivalent circuit of the second electrode 15 in this embodiment, and illustrates a display state in the case that white is displayed in upper right region AW and gray is displayed in the other region AG, which is

lapped over the equivalent circuit. In the equivalent circuit of the second electrode 15 of this embodiment, resistance elements are connected both in the column direction and the row direction in a lattice pattern. Therefore, voltage drop ΔV_1 in the white display region AW is approximately equal to voltage drop ΔV_2 in the gray display region AG ($\Delta V_1 \approx \Delta V_2$). Therefore, even gray is displayed.

[0140] Meanwhile, FIG. 26 illustrates an equivalent circuit of the second electrode 15 in the first embodiment, and illustrates a display state in the case that white is displayed in the upper right region AW and gray is displayed in the other region AG, which is lapped over the equivalent circuit. In the equivalent circuit of the second electrode 15 of the first embodiment, resistance elements are connected only in the column direction, and are not connected in the row direction. Therefore, the voltage drop ΔV_1 in the white display region AW is larger than the voltage drop ΔV_2 in the gray display region AG ($\Delta V_1 > \Delta V_2$). Therefore, the drive current I_{ds} flowing in the organic EL devices 10R, 10G, and 10B (see FIG. 27) is decreased above and below the white display region AW, and dark crosstalk CT occurs in the longitudinal direction with respect to the white display region AW. Luminance difference between the crosstalk CT and the normal gray display region AG is about 10%. The current I_{ds} is expressed by Mathematical expression 2.

[Mathematical Expression 2]

[0141]

$$I_{ds} = \left(V_{sig} - \frac{C_p}{C_s + C_p} \Delta V_{cath} \right)^2$$

[0142] FIG. 28 and FIG. 29 illustrate a simulation result of resistance R_1 in the column direction of the second electrode 15 and resistance R_2 in the row direction of the second electrode 15 in the case that the distance L in the column direction between the respective grooves 30 (see FIG. 24) and the thickness of the Mg—Ag alloy layer of the second electrode 15 are changed.

[0143] As can be seen from FIG. 29, in this embodiment in which the groove 30 is provided for each row of the plurality of organic EL devices 10R, 10G, and 10B, the resistance R_2 in the row direction of the second electrode 15 is remarkably decreased compared to in the first embodiment in which the groove 30 is continuously provided for a plurality of rows of the plurality of organic EL devices 10R, 10G, and 10B. That is, it is found that, in the case where the groove 30 is provided for each row of the plurality of organic EL devices 10R, 10G, and 10B in the column direction, the resistance R_2 in the row direction of the second electrode 15 is allowed to be decreased.

[0144] Further, comparing the case that the distance L in the column direction between the grooves 30 is 1.0 μm and the case that the distance L in the column direction between the grooves 30 is 2.4 μm , the resistance R_2 in the row direction of the second electrode 15 in the case that the distance L is 2.4 μm is higher. That is, it is found that, in the case where the distance L in the column direction between the grooves 30 is wider, the resistance R_2 in the row direction of the second electrode 15 is allowed to be decreased.

[0145] FIG. 30 illustrates a simulation result of a drop voltage in the case that the distance L in the column direction between the grooves 30 (see FIG. 24) and the thickness of the

Mg—Ag alloy layer of the second electrode **15** are changed as in FIG. 28. As the simulation conditions, it is assumed that a current value of the entire surface of the display region **110** is 15 mA, and only two blocks out of 25 blocks obtained by sectioning the display region **110** are lit. Therefore, flowing current is $15 \text{ mA} / 25 * 2 = 1.2 \text{ mA}$. The drop voltage is a difference between a voltage **V1** in the column in which the current flows and a voltage **V2** in the column in which the current does not flow (**V1**-**V2**). Further, in FIG. 30, the reference value at which crosstalk becomes visually tolerable level is 20 mV.

[0146] As can be seen from FIG. 30, in this embodiment in which the groove **30** is provided for each row of the plurality of organic EL devices **10R**, **10G**, and **10B**, the drop voltage is remarkably decreased compared to in the first embodiment in which the groove **30** is continuously provided for a plurality of rows of the plurality of organic EL devices **10R**, **10G**, and **10B**. That is, it is found that, in the case where the groove **30** is provided for each row of the plurality of organic EL devices **10R**, **10G**, and **10B** in the column direction, the drop voltage is allowed to be decreased, and deterioration in image quality is allowed to be suppressed.

[0147] Further, if comparison is made among the case that the distance **L** in the column direction between the grooves **30** is 1.0 μm , the case that the distance **L** in the column direction between the grooves **30** is 2.15 μm , and the case that the distance **L** in the column direction between the grooves **30** is 2.4 μm , the drop voltage is lower in the case that the distance **L** is wider. In the case where the distance **L** in the column direction between the respective grooves **30** is 1.0 μm , the thickness of the Mg—Ag alloy layer of the second electrode **15** may need to be equal to or larger than 4.6 nm in order to obtain crosstalk equal to or smaller than the reference value. Meanwhile, in the case where the distance **L** in the column direction between the grooves **30** is 2.15 μm and in the case where the distance **L** in the column direction between the grooves **30** is 2.4 μm , even if the thickness of the Mg—Ag alloy layer of the second electrode **15** is 4.0 nm, crosstalk is equal to or smaller than the reference value. That is, it is found that, in the case where the distance **L** in the column direction between the grooves **30** is wider, the drop voltage is allowed to be decreased to improve image quality.

Fourth Embodiment

[0148] FIG. 31 illustrates a cross-sectional configuration of part of a display region in a display unit according to a fourth embodiment of the present disclosure. The display unit according to the fourth embodiment has a similar configuration, a similar function, and a similar effect to those of the foregoing first to the third embodiments, except that the groove **30** is composed of, in a two-step manner, a first groove **31** of the first insulating film **21** and a second groove **32** of the second insulating film **22**, and is allowed to be manufactured in a similar manner to those of the foregoing first to third embodiments.

[0149] The first insulating film **21** has, for example, a thickness from 100 nm to 1000 nm both inclusive, and is composed of silicon oxide nitride (SiON) or silicon oxide (SiO₂ or SiO) as in the first embodiment.

[0150] In this embodiment, the second insulating film **22** is preferably composed of SiO₂ formed at high temperature for the following reason. That is, in the case where the film density of SiO₂ is increased, the rate at the time of etching is lower than that of the first insulating film **21**, and a canopy

section **32A** is formed. To be formed at high temperature herein specifically means that a film is formed at about from 400 deg C. to 500 deg C. both inclusive. SiO₂ of the first insulating film **21** is formed, for example, at from 250 deg C. to 350 deg C. both inclusive.

[0151] Width **w2** of the second groove **32** is narrower than width **w1** of the first groove **31**. In other words, the canopy section **32A** jetting toward the center of the first groove **31** is provided at the periphery of the second groove **32**. Thereby, as illustrated in FIG. 32, though the hole injection layer **14A** and the hole transport layer **14B** are attached to inside of the second groove **32**, the hole injection layer **14A** and the hole transport layer **14B** are totally cut at the borderline between the second groove **32** and the first groove **31**. Therefore, drive current leakage between the adjacent organic EL devices **10R**, **10G**, and **10B** is allowed to be suppressed. Further, in the first embodiment, in order to sufficiently decrease the thickness **t1** inside the groove **30** of the hole injection layer **14A** and the hole transport layer **14B**, the second groove **32** may need to have the depth **d** of several hundred μm . However, such necessity does not exist in this embodiment.

[0152] The width **w2** of the groove **32** is preferably, for example, equal to or smaller than the total film thickness of the organic layer **14**, and specifically, is preferably from 10 nm to 150 nm both inclusive. Thereby, as illustrated in FIG. 32, the light emitting layer **14C** is allowed to be continuous over the second groove **32**. As a result, the electron transport layer **14D** and the second electrode **15** are allowed to be continuous without being sectioned in parts over the groove **30**. Therefore, it is allowed to avoid an event that the second electrode **15** is cut by the groove **30**, and to provide the groove **30** not only in the column direction but also in the row direction in a lattice pattern.

[0153] The display unit is allowed to be manufactured, for example, as follows. For manufacturing steps similar to those of the first embodiment, a description will be given with reference to FIG. 9 to FIG. 15.

[0154] First, as illustrated in FIG. 9, the drive circuit layer **12** including the pixel drive circuit **140** and the first insulating film **21** composed of SiON or SiO₂ are formed over the substrate **11** as in the first embodiment.

[0155] Next, as illustrated in FIG. 10, the first electrode **13** is formed for each of the plurality of organic EL devices **10R**, **10G**, and **10B** on the first insulating film **21** as in the first embodiment.

[0156] Subsequently, as illustrated in FIG. 11, the second insulating film **22** composed of SiO₂ formed at high temperature is formed as in the first embodiment.

[0157] After that, the second groove **32** penetrating the second insulating film **22** is provided and the first groove **31** is provided in the first insulating film **21** by, for example, a photolithography method and dry etching.

[0158] After the second groove **32** is provided, further, wet etching of the first groove **31** and the second groove **32** is performed. As a chemical used for the wet etching, hydrofluoric acid is preferably used. Thereby, the width **w1** of the first groove **31** is increased in the first insulating film **21** in which the wet etching rate is high, while the width **w2** of the second groove **32** is narrower than the width **w1** of the first groove **31**, in the second insulating film **22** in which the wet etching rate is low. At the periphery of the second groove **32**, the canopy section **32A** jetting toward the center of the first groove **31** is formed.

[0159] Regarding the planar shape of the groove 30, it is possible that the groove 30 is provided continuously over a plurality of rows of the plurality of organic EL devices 10R, 10G, and 10B as in the first embodiment. Further, it is possible that the groove 30 is provided for each row of the plurality of organic EL devices 10R, 10G, and 10B as in the third embodiment.

[0160] After the step-like groove 30 having the first groove 31 and the second groove 32 is provided, as illustrated in FIG. 13 to FIG. 15, as in the first or the second embodiment, the hole injection layer 14A, the hole transport layer 14B, the light emitting layer 14C, and the electron transport layer 14D of the organic layer 14 are formed over the first electrode 13 and the second insulating film 22 by, for example, an evaporation method.

[0161] After the organic layer 14 is formed, as illustrated in FIG. 14 and FIG. 15, the second electrode 15 is formed as in the first embodiment. Thereby, the organic EL devices 10R, 10G, and 10B as illustrated in FIG. 3 and FIG. 4 are formed.

[0162] Subsequently, as illustrated in FIG. 4, the protective film 16 and the adhesive layer 17 made of the foregoing materials are formed on the organic EL devices 10R, 10G, and 10B as in the first embodiment. The resultant is bonded with the sealing substrate 40 provided with the color filter 41 and the light shielding film 42. Accordingly, the display unit illustrated in FIG. 1 to FIG. 4 is completed.

[0163] In the foregoing embodiment, the description has been given of the case that the side surface of the second groove 32 is a vertical surface. However, the side surface of the second groove 32 may be of an inverse-tapered shape as illustrated in FIG. 33 (the width of the second groove 32 becomes wider as the second groove 32 is deeper).

Fifth Embodiment

[0164] FIG. 34 illustrates a cross-sectional configuration of part of a display region in a display unit according to a fifth embodiment of the present disclosure. FIG. 35 illustrates an enlarged view of a section in the vicinity of the groove 30 illustrated in FIG. 34. In this embodiment, a conductive film 60 is provided on the bottom surface of the groove 30. By connecting the conductive film 60 to a determined electric potential, drive current leakage between the adjacent organic EL devices 10R, 10G, and 10B is almost totally suppressed. Further, in this embodiment, the second insulating film 22 is omitted, and only the first insulating film 21 is provided as the insulating film 20. Except for such points, the display unit of this embodiment has a similar configuration, a similar function, and a similar effect to those of the first to the third embodiments.

[0165] As described above, the conductive film 60 is connected to a determined electric potential such as a ground electric potential and an electric potential (cathode electric potential) of the second electrode 15. Thereby, a leakage current propagated through the hole injection layer 14A and the hole transport layer 14B is short-circuited to the conductive film 60, and a leakage current between the adjacent organic EL devices 10R, 10G, and 10B is almost totally suppressed. The second electrode 15 is connected to an auxiliary wiring (not illustrated) in a region outside the display region 110 for the following reason. That is, since the organic layer 14 is provided commonly to the plurality of organic EL devices 10R, 10G, and 10B, it is not possible to connect the second electrode 15 with the auxiliary wiring for each of the organic EL devices 10R, 10G, and 10B.

[0166] The conductive film 60 is made of a conductive material such as titanium nitride (TiN). The conductive film 60 is preferably a light shielding layer that blocks light entrance to the drive transistor Tr1 or the writing transistor Tr2 of the pixel drive circuit 140 of the drive circuit layer 12.

[0167] Regarding the planar shape of the groove 30, it is possible that the groove 30 is provided continuously over a plurality of rows of the plurality of organic EL devices 10R, 10G, and 10B as in the first embodiment. Further, it is possible that the groove 30 is provided for each row of the plurality of organic EL devices 10R, 10G, and 10B as in the third embodiment.

[0168] The display unit is allowed to be manufactured, for example, as follows. For manufacturing steps similar to those of the first embodiment, a description will be given with reference to FIG. 9 to FIG. 15.

[0169] First, as illustrated in FIG. 36A, the drive circuit layer 12 including the pixel drive circuit 140 is formed on the substrate 11 as in the first embodiment by the step illustrated in FIG. 9.

[0170] Next, as illustrated in FIG. 36B, an interlayer insulating film 61 is provided on the drive circuit layer 12, and the conductive film 60 made of the foregoing material is formed on the interlayer insulating film 61.

[0171] Subsequently, as illustrated in FIG. 36C, the first insulating film 21 having the contact hole 21A is formed on the interlayer insulating film 61 and the conductive film 60 as in the first embodiment by the step illustrated in FIG. 9. After that, as illustrated in FIG. 36C again, the plug 21B is formed in the contact hole 21A.

[0172] After the first insulating film 21 is formed, as illustrated in FIG. 36D, the first electrode 13 is formed for each of the plurality of organic EL devices 10R, 10G, and 10B on the first insulating film 21 as in the first embodiment by the step illustrated in FIG. 10.

[0173] After the first electrode 13 is formed, as illustrated in FIGS. 37A and 37B, the groove 30 is provided in the first insulating film 21 by a photolithography method and etching so that the bottom surface of the groove 30 reaches the conductive film 60.

[0174] After the groove 30 is provided, as illustrated in FIG. 38, the hole injection layer 14A, the hole transport layer 14B, the light emitting layer 14C, and the electron transport layer 14D of the organic layer 14 are formed over the first electrode 13 and the second insulating film 22 by, for example, an evaporation method as in the first or the second embodiment by the steps illustrated in FIG. 13 to FIG. 15.

[0175] After the organic layer 14 is formed, as illustrated in FIG. 14 and FIG. 15, the second electrode 15 is formed as in the first embodiment. Thereby, the organic EL devices 10R, 10G, and 10B as illustrated in FIG. 3 and FIG. 4 are formed.

[0176] Subsequently, as illustrated in FIG. 4, the protective film 16 and the adhesive layer 17 made of the foregoing materials are formed on the organic EL devices 10R, 10G, and 10B as in the first embodiment. The resultant is bonded with the sealing substrate 40 provided with the color filter 41 and the light shielding film 42. Accordingly, the display unit illustrated in FIG. 1 to FIG. 4 is completed.

Module and Application Examples

[0177] A description will be given of application examples of the display unit described in the foregoing embodiments. The display unit of the foregoing embodiments is applicable to a display unit of an electronic device in any field for

displaying a video signal inputted from outside or a video signal generated inside as an image or a video such as a television device, a digital camera, a notebook personal computer, a portable terminal device such as a mobile phone, and a video camcorder.

Module

[0178] The display unit of the foregoing embodiment is incorporated in electronic devices such as after-mentioned first and second application examples as a module as illustrated in FIG. 39, for example. In the module, for example, a region 310 exposed from the sealing substrate 40 is provided on a side of the substrate 11, and an external connection terminal (not illustrated) is formed in the exposed region 310 by extending the wirings of the signal line drive circuit 120 and the scanning line drive circuit 130. The external connection terminal may be provided with a flexible printed circuit (FPC) 320 for inputting and outputting a signal.

First Application Example

[0179] FIGS. 40A and 40B illustrate an appearance of an image pickup device (single-lens reflex digital camera with interchangeable lenses) to which the display unit of the foregoing embodiments is applied. The image pickup device has, for example, an interchangeable image pickup lens unit (interchangeable lens) 412 on the front right side of a camera body 411 and a grip section 413 to be gripped by a photographer on the front left side. In the approximately central section of the rear surface of the camera body 411, a monitor 414 is provided. A viewfinder (eyepiece window) 415 is provided above the monitor 414. The photographer is able to view a subject light figure guided from the image pickup lens unit 412 and determine a picture composition by viewing the image on the viewfinder 415. The viewfinder 415 is composed of the display unit according to the foregoing embodiments.

Second Application Example

[0180] FIG. 41 illustrates an appearance of a head-mounted display to which the display unit of the foregoing embodiments is applied. The head-mounted display has, for example, an ear-hook section 422 to be worn on a user's head on both sides of an eyeglass-shaped display section 421. The display section 421 is composed of the display unit according to the foregoing embodiments.

[0181] While the present disclosure has been described with reference to the embodiments, the present disclosure is not limited to the foregoing embodiments, and various modifications may be made. For example, in the foregoing embodiments, the description has been given of the case that a leakage current between the organic light emitting devices 10R, 10G, and 10B is suppressed by decreasing the thickness inside the groove 30 of the hole injection layer 14A and the hole transport layer 14B. However, it is enough that at least the thickness inside the groove 30 of the hole injection layer 14A or the hole transport layer 14B is decreased. Further, out of the organic layer 14, conductivity of the hole injection layer 14A is particularly high. Therefore, a leakage current between the organic light emitting devices 10R, 10G, and 10B is allowed to be decreased by decreasing at least the thickness inside the groove 30 of the hole injection layer 14A. Further, in the case where the hole transport layer 14B is omitted, a leakage current between the organic light emitting

devices 10R, 10G, and 10B is allowed to be decreased by decreasing the thickness inside the groove 30 of the hole injection layer 14A.

[0182] Further, in the foregoing embodiments, the description has been given of the case that light generated in the light emitting layer is extracted from the second electrode 15 side (top emission) as an example. However, it is possible that light generated in the light emitting layer is extracted from the first electrode 13 side (bottom emission). In this case, the first electrode 13 is formed of a transparent electrode made of ITO, IZO (registered trademark), SnO₂, or the like. The second electrode 15 is formed of a reflecting electrode composed of a simple body or an alloy of a metal element such as gold (Au), platinum (Pt), nickel (Ni), chromium (Cr), copper (Cu), tungsten (W), aluminum (Al), molybdenum (Mo), and silver (Ag). Further, the second electrode 15 may be composed of a composite film of the foregoing reflecting electrode and the foregoing transparent electrode. Further, in the case of the bottom emission type, the color filter 41 and the light shielding film 42 may be provided on the substrate 11 side, for example, may be provided between the drive circuit layer 12 and the first insulating film 21.

[0183] Further, for example, the material, the thickness, the film-forming method, the film-forming conditions, and the like of each layer are not limited to those described in the foregoing embodiments, and other material, other thickness, other film-forming method, and other film-forming conditions may be adopted.

[0184] It is possible to achieve at least the following configurations from the above-described exemplary embodiments and the modifications of the disclosure.

(1) A display unit comprising, on a substrate:

[0185] a plurality of organic EL devices; and

[0186] an insulating film provided in an inter-device region between the plurality of organic EL devices, the insulating film including a groove in a position between the organic EL devices adjacent to each other.

(2) The display unit according to (1), wherein the organic EL device includes

[0187] a first electrode provided for each of the plurality of organic EL devices,

[0188] an organic layer being provided on the first electrode and the insulating film commonly to the plurality of organic EL devices, and including a hole injection layer or a hole transport layer and a light emitting layer, and

[0189] a second electrode being provided on the organic layer commonly to the plurality of organic EL devices, and

[0190] thickness inside the groove of the hole injection layer or of the hole transport layer is smaller than thickness outside the groove thereof.

(3) The display unit according to (2), wherein the thickness inside the groove of the hole injection layer or of the hole transport layer becomes smaller toward a depth direction of the groove.

(4) The display unit according to any one of (1) to (3), wherein the plurality of organic EL devices have a rectangle shape extending in one direction, and are arranged in a row direction parallel with a short side thereof and in a column direction parallel with a long side thereof, and

[0191] the groove is provided continuously over a plurality of rows of the plurality of organic EL devices in the column direction.

(5) The display unit according to any one of (1) to (3), wherein the plurality of organic EL devices have a rectangle shape, and are arranged in a row direction parallel with a short side thereof and in a column direction parallel with a long side thereof, and

[0192] the groove is provided for each row of the plurality of organic EL devices in the column direction.

(6) The display unit according to (4) or (5), wherein pitch in the row direction of the plurality of organic EL devices is equal to or smaller than about 30 μm .

(7) The display unit according to any one of (1) to (6), wherein the insulating film includes a first insulating film and a second insulating film, the first insulating film being provided between the substrate and the plurality of organic EL devices, and the second insulating film being provided in the inter-device region on the first insulating film, and

[0193] the groove includes a first groove and a second groove, the first groove being provided in the first insulating film, and the second groove being provided in the second insulating film, communicating with the first groove, and having width narrower than width of the first groove.

(8) The display unit according to any one of (2) to (6), including a conductive film on a bottom surface of the groove,

[0194] wherein the conductive film is connected to a determined electric potential.

(9) The display unit according to (8), wherein the determined electric potential is a ground electric potential or an electric potential of the second electrode.

(10) The display unit according to (8) or (9) comprising:

[0195] a drive circuit including a transistor between the substrate and the plurality of organic EL devices, and between the substrate and the insulating film,

[0196] wherein the conductive film is a light shielding layer.

(11) The display unit according to any one of (1) to (10), wherein the light emitting layer is a white light emitting layer, and

[0197] the organic EL device includes a color filter extracting the white light as red light, green light, or blue light.

(12) A method of manufacturing a display unit, the method comprising:

[0198] forming a plurality of organic EL devices on a substrate; and

[0199] forming an insulating film in an inter-device region between the plurality of organic EL devices,

[0200] wherein in the forming of the insulating film, a groove is provided in a position between the organic EL devices adjacent to each other of the insulating film.

(13) The method of manufacturing a display unit according to (12), wherein the forming of the plurality of organic EL devices includes

[0201] forming a first electrode for each of the plurality of organic EL devices,

[0202] forming an organic layer including a hole injection layer or a hole transport layer and a light emitting layer commonly to the plurality of organic EL devices on the first electrode and the insulating film, and

[0203] forming a second electrode commonly to the plurality of organic EL devices on the organic layer, and

[0204] wherein the forming of the organic layer is performed after providing the groove in the insulating film.

(14) The method of manufacturing a display unit according to (13), wherein in the forming of the organic layer is performed by an evaporation method, and Mathematical expression 1 is satisfied,

$$X/Y > w/d$$

(Mathematical expression 1)

[0205] where X represents offset distance from an entrance edge of the groove to an evaporation source, Y represents distance between the substrate and the evaporation source, w represents width of the groove, and d represents depth of the groove.

(15) The method of manufacturing a display unit according to (14), wherein in the forming of the organic layer, the evaporation method is a rotary evaporation method in which film formation is performed while the substrate is rotated, and the Mathematical expression 1 is satisfied during part or all of a time period in which the substrate is rotated once.

(16) The method of manufacturing a display unit according to (14), wherein in the forming of the organic layer, the evaporation method is a linear evaporation method in which film formation is performed while the evaporation source and the substrate are relatively moved in one direction, and the Mathematical expression 1 is satisfied during part or all of a time period in which the substrate passes the evaporation source.

(17) The method of manufacturing a display unit according to any one of (14) to (16), wherein in the forming of the hole injection layer or the hole transport layer out of the organic layer, the Mathematical expression 1 is satisfied.

[0206] It should be understood that various changes and modifications to the presently preferred embodiments described herein will be apparent to those skilled in the art. Such changes and modifications can be made without departing from the spirit and scope of the present subject matter and without diminishing its intended advantages. It is therefore intended that such changes and modifications be covered by the appended claims.

The invention is claimed as follows:

1. A display unit comprising, on a substrate:

a plurality of organic EL devices; and

an insulating film provided in an inter-device region between the plurality of organic EL devices, the insulating film including a groove in a position between the organic EL devices adjacent to each other.

2. The display unit according to claim 1, wherein the organic EL device includes

a first electrode provided for each of the plurality of organic EL devices,

an organic layer being provided on the first electrode and the insulating film commonly to the plurality of organic EL devices, and including a hole injection layer or a hole transport layer and a light emitting layer, and

a second electrode being provided on the organic layer commonly to the plurality of organic EL devices, and thickness inside the groove of the hole injection layer or of the hole transport layer is smaller than thickness outside the groove thereof.

3. The display unit according to claim 2, wherein the thickness inside the groove of the hole injection layer or of the hole transport layer becomes smaller toward a depth direction of the groove.

4. The display unit according to claim 1, wherein the plurality of organic EL devices have a rectangle shape extending in one direction, and are arranged in a row direction parallel with a short side thereof and in a column direction parallel with a long side thereof, and the groove is provided continuously over a plurality of rows of the plurality of organic EL devices in the column direction.

5. The display unit according to claim 1, wherein the plurality of organic EL devices have a rectangle shape, and are arranged in a row direction parallel with a short side thereof and in a column direction parallel with a long side thereof, and the groove is provided for each row of the plurality of organic EL devices in the column direction.

6. The display unit according to claim 4, wherein pitch in the row direction of the plurality of organic EL devices is equal to or smaller than about 30 μm .

7. The display unit according to claim 1, wherein the insulating film includes a first insulating film and a second insulating film, the first insulating film being provided between the substrate and the plurality of organic EL devices, and the second insulating film being provided in the inter-device region on the first insulating film, and the groove includes a first groove and a second groove, the first groove being provided in the first insulating film, and the second groove being provided in the second insulating film, communicating with the first groove, and having width narrower than width of the first groove.

8. The display unit according to claim 2, including a conductive film on a bottom surface of the groove, wherein the conductive film is connected to a determined electric potential.

9. The display unit according to claim 8, wherein the determined electric potential is a ground electric potential or an electric potential of the second electrode.

10. The display unit according to claim 8 comprising: a drive circuit including a transistor between the substrate and the plurality of organic EL devices, and between the substrate and the insulating film, wherein the conductive film is a light shielding layer blocking light.

11. The display unit according to claim 1, wherein the light emitting layer is a white light emitting layer, and the organic EL device includes a color filter extracting the white light as red light, green light, or blue light.

12. A method of manufacturing a display unit, the method comprising:

forming a plurality of organic EL devices on a substrate; and

forming an insulating film in an inter-device region between the plurality of organic EL devices, wherein in the forming of the insulating film, a groove is provided in a position between the organic EL devices adjacent to each other of the insulating film.

13. The method of manufacturing a display unit according to claim 12, wherein the forming of the plurality of organic EL devices includes forming a first electrode for each of the plurality of organic EL devices, forming an organic layer including a hole injection layer or a hole transport layer and a light emitting layer commonly to the plurality of organic EL devices on the first electrode and the insulating film, and forming a second electrode commonly to the plurality of organic EL devices on the organic layer, and wherein the forming of the organic layer is performed after providing the groove in the insulating film.

14. The method of manufacturing a display unit according to claim 13, wherein in the forming of the organic layer is performed by an evaporation method, and Mathematical expression 1 is satisfied,

$$X/Y > w/d \quad (\text{Mathematical expression 1})$$

where X represents offset distance from an entrance edge of the groove to an evaporation source, Y represents distance between the substrate and the evaporation source, w represents width of the groove, and d represents depth of the groove.

15. The method of manufacturing a display unit according to claim 14, wherein in the forming of the organic layer, the evaporation method is a rotary evaporation method in which film formation is performed while the substrate is rotated, and the Mathematical expression 1 is satisfied during part or all of a time period in which the substrate is rotated once.

16. The method of manufacturing a display unit according to claim 14, wherein in the forming of the organic layer, the evaporation method is a linear evaporation method in which film formation is performed while the evaporation source and the substrate are relatively moved in one direction, and the Mathematical expression 1 is satisfied during part or all of a time period in which the substrate passes the evaporation source.

17. The method of manufacturing a display unit according to claim 14, wherein in the forming of the hole injection layer or the hole transport layer out of the organic layer, the Mathematical expression 1 is satisfied.

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摘要(译)

显示单元在基板上包括多个有机EL器件，以及设置在多个有机EL器件之间的器件间区域中的绝缘膜，该绝缘膜包括位于相邻的有机EL器件之间的位置的凹槽对彼此。

